

E6D30065H

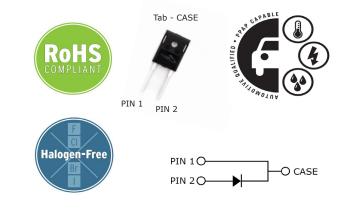
E-Series Automotive 650 V, 30 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Automotive Qualified (AEC Q101) and PPAP Capable



Part Number	Package	Marking
E6D30065H	TO-247-2	E6D30065H

Typical Applications

- Interleaved or Bridgless PFC
- DC/DC On Board Battery Chargers
- Boost for PFC & DC-DC Stages
- AC/DC On Board Chargers
- PFC Output Rectification

Maximum Ratings ($T_c = 25^{\circ}C$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V _{RRM}	650	_		
Surge Peak Reverse Voltage	V _{RSM}	650	V		
DC Blocking Voltage	V _{DC}	650			
		84		$T_c = 25 \text{ °C}$	
Continuous Forward Current	I _F	42		T _c = 125 °C	Fig. 3
		26	A	T _c = 150 °C	
Repetitive Peak Forward Surge		112		$T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
Current	FRM	64		$T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Forward Surge		207		$T_c = 25 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
Current			A	$T_c = 110 \text{ °C}, t_p = 10 \text{ ms}, \text{Half Sine Wave}$	
		211		$T_c = 25 \text{ °C}$	
Power Dissipation	P _{tot}	91.5	W	T _c = 110 °C	Fig. 4
·21	62.4	214	• • • •	$T_{c} = 25 \text{ °C}, t_{p} = 10 \text{ ms}$	
i²t value	∫i²dt	175	A ² s	$T_{c} = 110 \text{ °C}, t_{p} = 10 \text{ ms}$	

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Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
E IVI		1.35	1.5		I _F = 30 A, T _j = 25 °C	F. 1
Forward Voltage	V _F	1.45	1.6	V	I _F = 30 A, T _j = 175 °C	Fig. 1
Deverse Comment		10	100	μΑ	V _R = 650 V, T _j = 25 °C	Fig. 2
Reverse Current	I _R	45	350		V _R = 650 V, T _j = 175 °C	
Total Capacitive Charge	Q _c	98		nC	$V_{R} = 400 \text{ V}, \text{ T}_{j} = 25 \text{ °C}$	Fig. 5
		1850			$V_{R} = 0 V, T_{j} = 25 °C, f = 1 MHz$	
Total Capacitance	С	187		pF	$V_{R} = 200 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$	Fig. 6
		143			$V_{R} = 400 \text{ V}, \text{ T}_{j} = 25 \text{ °C}, \text{ f} = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	14.6		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	0.55	°C/W	
Thermal Resistance, Junction to Case (Max)	R _{0, JC (MAX)}	0.71	°C / W	
Junction Temperature	Tj	-55 to +175	°C	
Case & Storage Temperature	T _c	-55 to +175		
		1	Nm	M3 Screw
TO-247 Mounting Torque	-	8.8	lbf-in	6-32 Screw

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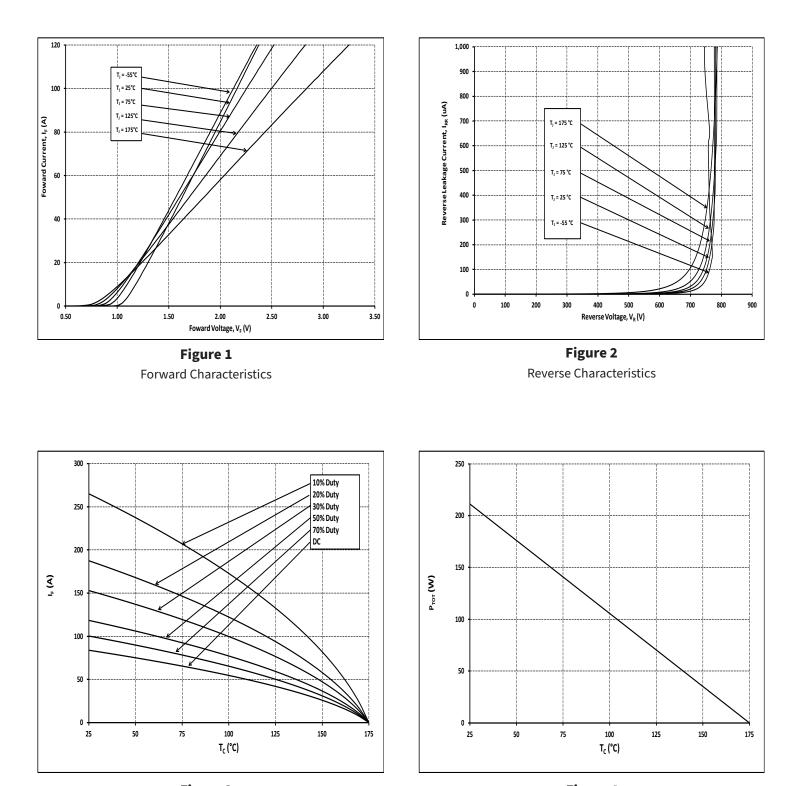
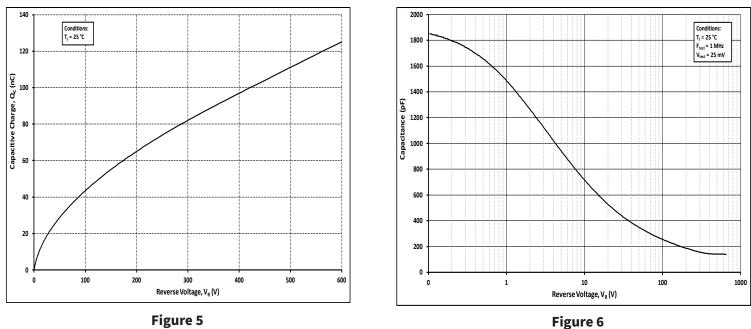


Figure 3 Current Derating **Figure 4** Power Derating

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Total Capacitance vs. Reverse Voltage

Capacitace vs. Reverse Voltage

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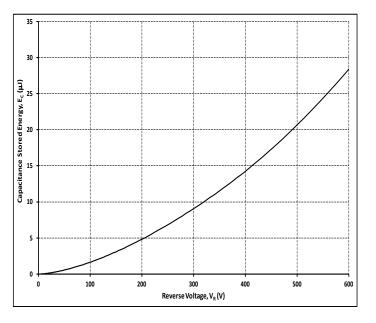
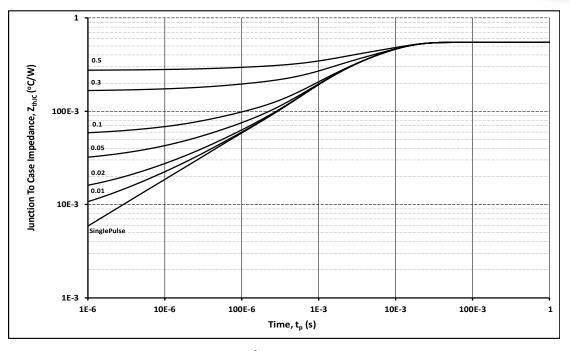
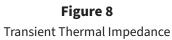


Figure 7 Capacitance Stored Energy

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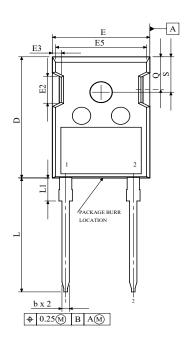


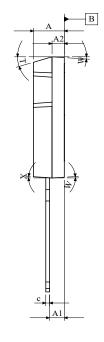
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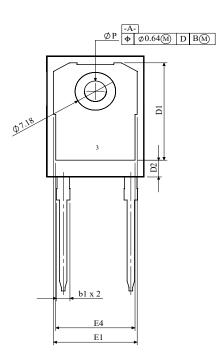
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Package Dimensions & Pin-Out

Package: TO-247-2







SYMBOL	MIN (mm)	MAX (mm)		
Α	4.86	5.21		
A1	2.29	2.54		
A2	1.91	2.16		
b	1.07	1.33		
b1	1.91	2.41		
с	0.55	0.68		
D	20.80	21.10		
D1	16.25	17.35		
D2	2.86	3.16		
E	15.75	16.13		
E1	13.10	14.15		
E2	3.68	5.10		
E3	1.00	1.90		
E4	12.38	13.43		
E5	14.65	15.05		
e	10.88	BSC		
L	19.81	20.32		
L1	4.10	4.40		
ØР	3.51	3.65		
Q	5.49	6.00		
S	6.04	6.30		
Т	17.5° REF			
W	3.5 ° REF			
X	4° REF			

1	CATHODE
2	ANODE
3	CATHODE

NOTE

- 1. ALL METAL SURFACES ARE TIN PLATED (MATTE), EXCEPT AREA OF CUT.
- 2. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
- 3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
- 4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS

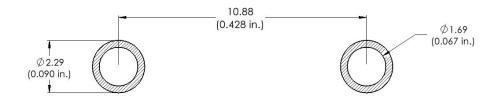
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Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
E6D30065H	Tube

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Revision History

Document Version	Date of Release	Description of Changes
1	January 2024	Initial Release
2	February - 2025	Legal disclaimer Updated

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