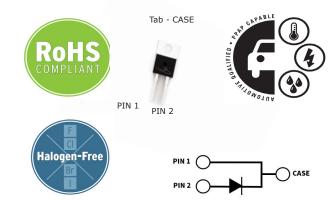


E-Series Automotive 650 V, 30 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Part Number	Package	Marking	
E6D30065A	TO-220-2	E6D30065A	

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Automotive Qualified (AEC Q101) and PPAP Capable

Typical Applications

- Interleaved or Bridgless PFC
- DC/DC On Board Battery Chargers
- Boost for PFC & DC-DC Stages
- AC/DC On Board Chargers
- PFC Output Rectification

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V _{RRM}	650			
Surge Peak Reverse Voltage	V _{RSM}	650	V		
DC Blocking Voltage	V _{DC}	650			
		92.5		T _c = 25 °C	
Continuous Forward Current	I _F	46		T _c = 125 °C	Fig. 3
		29	Α	T _c = 150 °C	
Repetitive Peak Forward Surge		110		T _c = 25 °C, t _p = 10 ms, Half Sine Wave	
Current	FRM	62		$T_c = 110 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	
Non-Repetitive Forward Surge		202		T _c = 25 °C, t _p = 10 ms, Half Sine Wave	
Current	FSM	176	A	$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$	
2		242		T _c = 25 °C	
Power Dissipation	P _{tot}	105	W	T _c = 110 °C	Fig. 4
*21	63.11	204	A 2	$T_c = 25 {}^{\circ}\text{C}, t_p = 10 \text{ms}$	
i²t value	∫i²dt	139	A ² s	$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$	

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Famous ad Malda as		1.3	1.5		I _F = 30 A, T _j = 25 °C	F:_ 1
Forward Voltage	V _F	1.45	1.6	V	I _F = 30 A, T _j = 175 °C	Fig. 1
D C 1		10	100		$V_R = 650 \text{ V}, T_j = 25 \text{ °C}$	F. 0
Reverse Current	I _R	22	350	μΑ	$V_R = 650 \text{ V}, T_j = 175 ^{\circ}\text{C}$	Fig. 2
Total Capacitive Charge	Q _c	97		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		1864			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	185		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		142			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	14.4		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	0.48	°C/W	
Thermal Resistance, Junction to Case (Max)	R _{0, JC (MAX)}	0.62	°C/W	
Junction Temperature	T _j	-55 to +175	°C	
Case & Storage Temperature	T _c	-55 to +175		
		1	1 Nm M3	
TO-220 Mounting Torque	-	8.8	lbf-in	6-32 Screw

Typical Performance

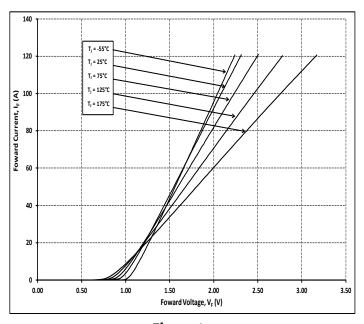
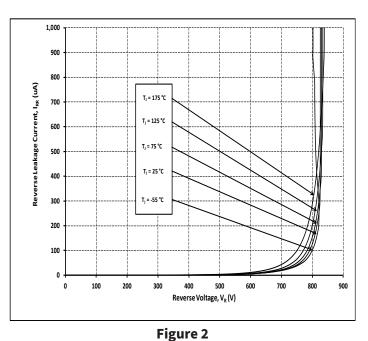


Figure 1Forward Characteristics



Reverse Characteristics

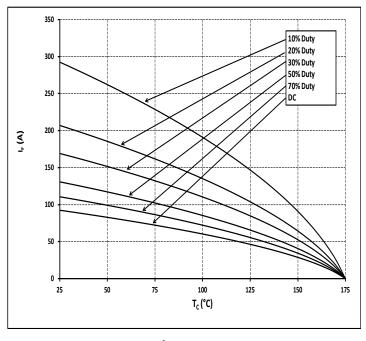


Figure 3Current Derating

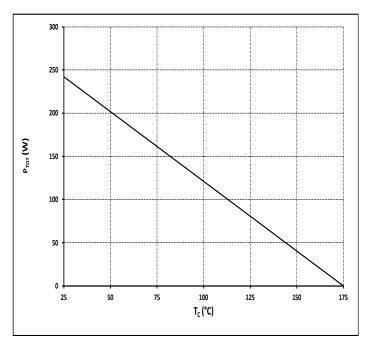


Figure 4Power Derating

2000

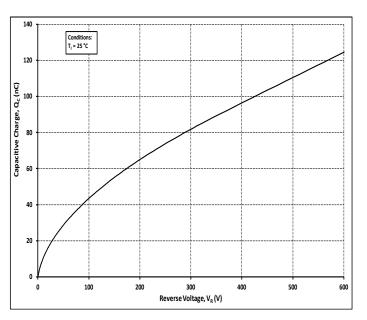
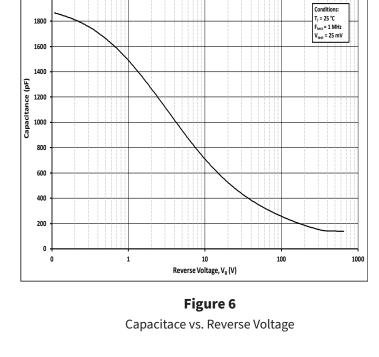


Figure 5Total Capacitance vs. Reverse Voltage



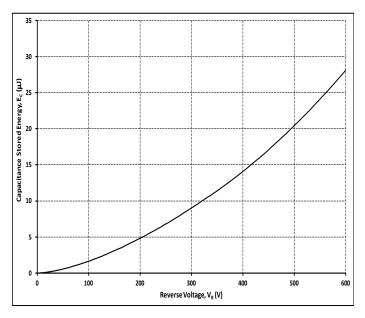


Figure 7Capacitance Stored Energy

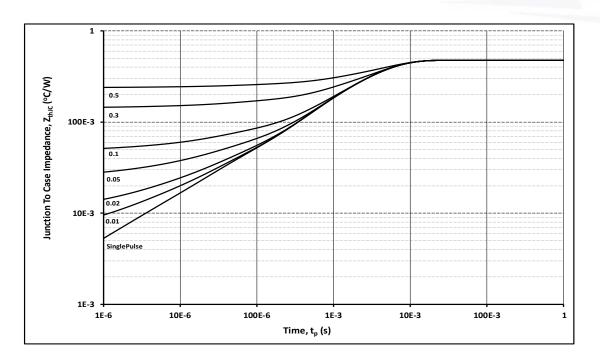
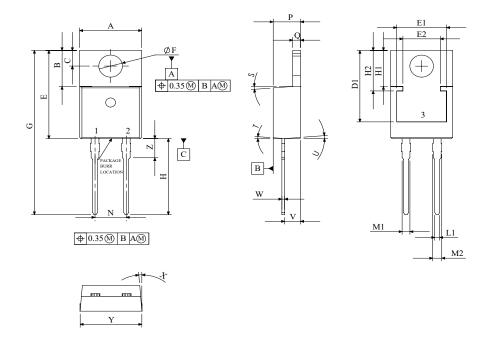


Figure 8Transient Thermal Impedance

Package Dimensions & Pin-Out

Package: TO-220-2



SYMBOL	MIN (mm)	MAX (mm)	
A	9.67	10.41	
В	5.96	6.48	
C	2.54	3.05	
D1	12.45	REF	
E	14.98	15.62	
E1	8.12	REF	
E2	6.10	REF	
F	3.63	3.89	
G	28.06	29.13	
H	12.70	13.97	
H1	6.22 REF		
H2	7.04 REF		
L1	0.63	0.91	
M1	1.14	1.40	
M2	1.14	1.77	
N	4.95	5.21	
P	4.19	4.70	
Q	1.21	1.37	
S	3°	6°	
T	3°	6°	
U	3°	6°	
V	2.38	2.79	
W	0.35	0.64	
X	3°	5.5°	
Y	9.77	10.41	
Z	3.30	3.81	

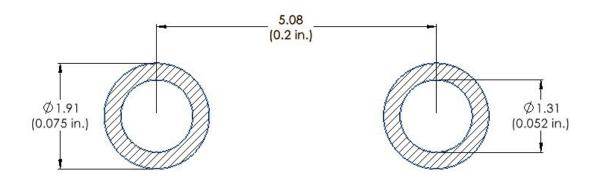
1	CATHODE	
2	ANODE	
3	CATHODE	

NOTE

- $\begin{array}{l} {\rm 1.\;ALL\;METAL\;SURFACES\;ARE\;TIN\;PLATED\;(MATTE),} \\ {\rm EXCEPT\;AREA\;OF\;CUT.} \end{array}$
- $\begin{tabular}{ll} 2. \ DIMENSIONING \& \ TOLERANCING \ CONFORM \ TO \\ ASME \ Y14.5M-1994. \end{tabular}$
- 3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
- 4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS

Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type	
E6D30065A	Tube	

Revision History

Document Version	Date of Release	Description of Changes
1	January 2024	Initial Release
2	February - 2025	Legal Disclaimer, updated POD



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