

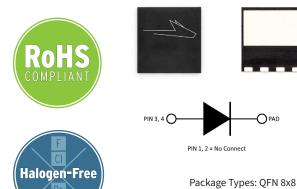
Marking: C6D06065Q

C6D06065Q

6th Generation 650 V, 6 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Low Profile Package with Low Inductance

Typical Applications

- Enterprise Power, Server, & Telecom Power Supplies
- Switched Mode Power Supplies
- Industrial Power Supplies
- Boost Power Factor Correction
- Bootstrap Diode
- LLC Clamping

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	650	V			
DC Blocking Voltage	V _{DC}	650	V			
		25		T _J = 25 °C		
Continuous Forward Current	I _F	13		T _J = 125 °C	Fig. 3	
		7	А	T _J = 155 °C	l	
Non-Repetitive Peak Forward	I _{FSM}	48		$T_c = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}, Half Sine Wave}$		
Surge Current		42		$T_c = 110 {}^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
Power Dissipation	P _{tot}	77	W	T _c = 25 °C	Fig. 4	
		33		T _c = 110 °C		
i²t Value	∫i²t	11	A ² s	$T_{c} = 25 ^{\circ}\text{C}, t_{p} = 10 \text{ms}$		
		8		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \text{ms}$		

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Famurayal Valkana	V	1.27	1.5	V	I _F = 6 A, T _j = 25 °C	Fig. 1
Forward Voltage	V _F	1.37	1.6		I _F = 6 A, T _j = 175 °C	
Reverse Current		2	20	μΑ	$V_{R} = 650 \text{ V}, T_{j} = 25 ^{\circ}\text{C}$	Fig. 2
	l R	25	200		$V_R = 650 \text{ V}, T_j = 175 \text{ °C}$	
Total Capacitive Charge	Q_c	22		nC	$V_{R} = 400 \text{ V}, T_{j} = 25 \text{ °C}$	Fig. 5
Total Capacitance		393		pF	$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
	С	44			$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		36			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	3.5		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typ.)	$R_{\theta, JC}$	2.0	°C/W	
Junction Temperature	T _j	-55 to +175		
Case & Storage Temperature	T _c	-55 to +150	°C	
Maximum Processing Temperature	T _{PROC}	325		10 min max.

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Notes
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

Typical Performance

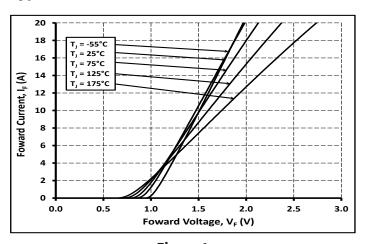
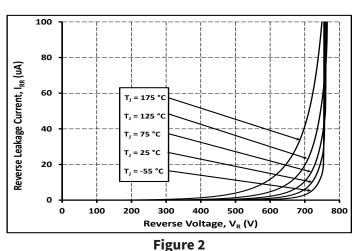


Figure 1Forward Characteristics



Reverse Characteristics

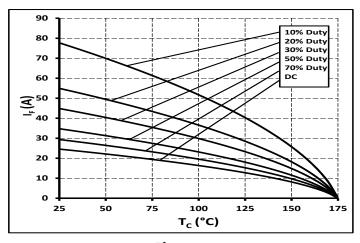
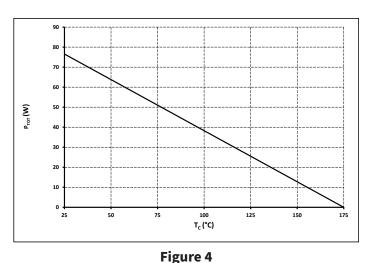
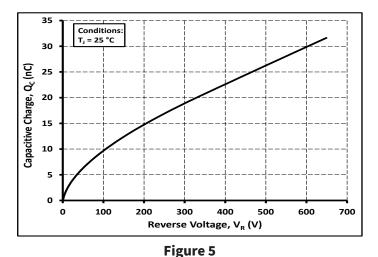


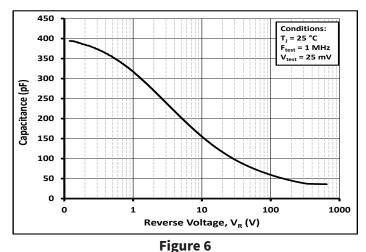
Figure 3
Current Derating



Power Derating



Total Capacitance Charge vs. Reverse Voltage



Capacitance vs. Reverse Voltage

Typical Performance

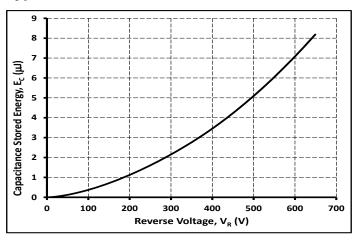


Figure 7Capacitance Stored Energy

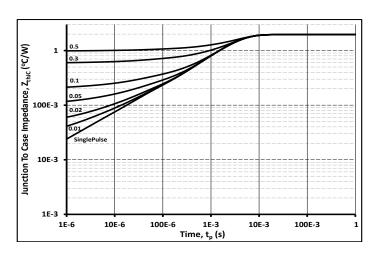
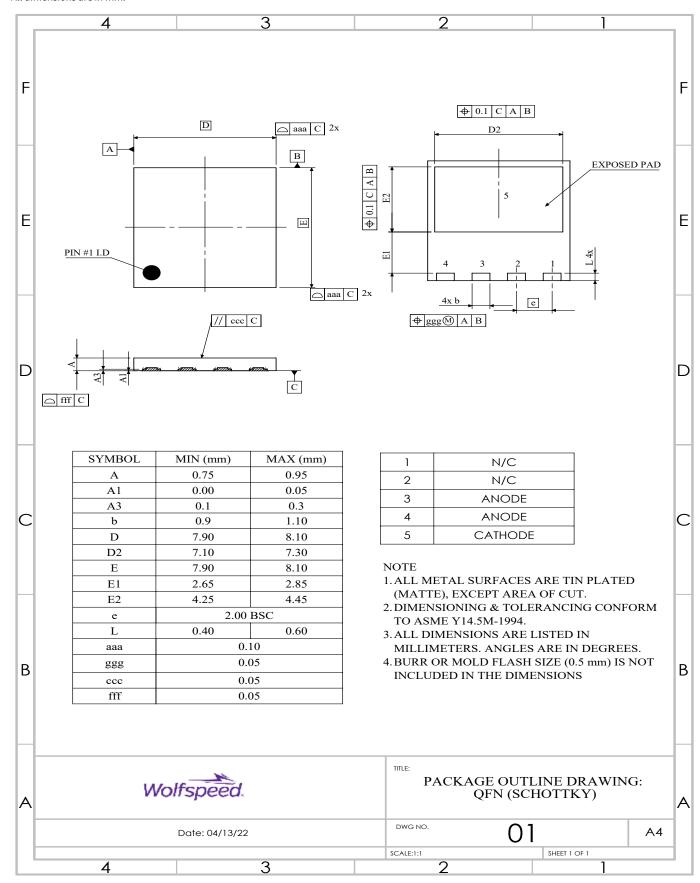


Figure 8Transient Thermal Impedence

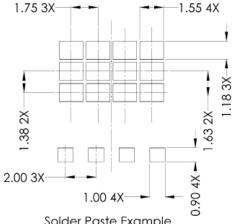
Package Dimensions & Pin-Out

All dimensions are in mm.

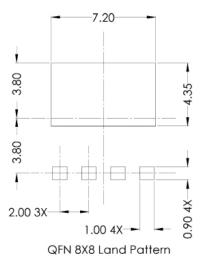


Recommended Solder Pad Layout

Learn more about recommended soldering profiles in this application note.



Solder Paste Example
Based on 5mil thick stencil
70% printed solder coverage by thermal pad area



Product Ordering Information

Order Number	Packing Type
C6D06065Q-TR	Tape & Reel

 $Learn\ more\ about\ power\ device\ packing\ \&\ shipment\ information\ in\ \underline{this\ application\ note}.$

REACh, RoHS, and Halogen-Free compliance documentation available for this product.

Revision History

Current Revision	Date of Release	Description of Changes			
1	November - 2024	Legal Disclaimer Updated			
2	April - 2025	$\rm R_{th}$ updated to reflect new data, $\rm I_{F}$, $\rm P_{tot}$, Fig 3, 4, and 8 updated to reflect the $\rm R_{th}$ update			

Notes & Disclaimer

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