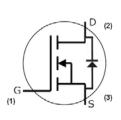


Silicon Carbide Power MOSFET C3M™ MOSFET Technology N-Channel Enhancement Mode

#### **Features**

- C3M SiC MOSFET technology
- High blocking voltage with low On-resistance
- High speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q<sub>rr</sub>)
- Halogen free, RoHS compliant









Part Number	Package	Marking
C3M0120090D	TO 247-3	C3M0120090

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## **Applications**

- Renewable energy
- EV battery chargers
- High voltage DC/DC converters
- Switch Mode Power Supplies
- Lighting

#### **Benefits**

- Higher system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency

#### **Key Parameters**

Parameter	Symbol	Min.	Тур.	Max	Unit	Conditions	Note
Drain - Source Voltage	V <sub>DS</sub>			900	V	T <sub>c</sub> = 25°C	
Maximum Gate - Source Voltage	V <sub>GS(max)</sub>	-8		+19	]	Transient	
Operational Gate-Source Voltage	V <sub>GS op</sub>		-4/15			Static	Note 1
DC Continuous Drain Current	I <sub>D</sub>			23		$V_{GS} = 15 \text{ V}, T_{C} = 25 \text{ °C}, T_{J} \le 150 \text{ °C}$	Fig. 19 Note 2
				15	A	$V_{GS} = 15 \text{ V}, T_{C} = 100 \text{ °C}, T_{J} \le 150 \text{ °C}$	
Pulsed Drain Current	I <sub>DM</sub>			50		t <sub>Pmax</sub> limited by T <sub>jmax</sub> V <sub>GS</sub> = 15V, T <sub>C</sub> = 25 °C	Fig. 22
Power Dissipation	P <sub>D</sub>			97	w	$T_{c} = 25^{\circ}C, T_{J} = 150^{\circ}C$	Fig. 20
Operating Junction and Storage Temperature	$T_J$ , $T_{stg}$			-55 to +150	°C		
Solder Temperature	T <sub>L</sub>			260		According to JEDEC J-STD-020	
Mounting Torque	M <sub>D</sub>			1 8.8	Nm Ibf-in	M3 or 6-32 screw	

 $Note~(1): Recommended~turn-on~gate~voltage~is~15V~with~\pm 5\%~regulation~tolerance, see~Application~Note~PRD-04814~for~additional~details~tolerance, see~Application~details~tolerance, see~Applicat$ 

Note (2): Verified by design

## **Electrical Characteristics** ( $T_c = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	<b>Test Conditions</b>	Note	
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	900	_	_		$V_{GS} = 0 \text{ V}, I_{D} = 100 \mu\text{A}$		
Cata Thursday I d Valta as	.,,	1.8	2.1	3.5	V	$V_{DS} = V_{GS}$ , $I_D = 3 \text{ mA}$	F: 11	
Gate Threshold Voltage	$V_{GS(th)}$	_	1.6	_		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 3 mA, T <sub>J</sub> = 150°C	Fig. 11	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	_	1	100	μΑ	V <sub>DS</sub> = 900 V, V <sub>GS</sub> = 0 V		
Gate-Source Leakage Current	I <sub>GSS</sub>	_	10	250	nA	V <sub>GS</sub> = 15 V, V <sub>DS</sub> = 0 V		
Durin Course On Chata Basistan	_	_	120	155	0	V <sub>GS</sub> = 15 V, I <sub>D</sub> = 15 A	Fig.	
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	_	170	_	mΩ	V <sub>GS</sub> = 15 V, I <sub>D</sub> = 15 A, T <sub>J</sub> = 150°C	4, 5, 6	
Toron and destance	_		8.9	_	V <sub>DS</sub> = 20 V, I <sub>DS</sub> = 15 A	F: -7		
Transconductance	<b>g</b> fs	_	7.1	_	S	$V_{DS} = 20 \text{ V}, I_{DS} = 15 \text{ A}, T_{J} = 150^{\circ}\text{C}$	Fig. 7	
Input Capacitance	C <sub>iss</sub>	_	414	_			Fig. 17, 18	
Output Capacitance	Coss	_	48	_	pF	$V_{GS} = 0 \text{ V}, V_{DS} = 600 \text{ V}$		
Reverse Transfer Capacitance	C <sub>rss</sub>	_	3	_		$ \int_{AC} f = 1 \text{ Mhz} $ $ V_{AC} = 25 \text{ mV} $		
Output Capacitance Stored Energy	E <sub>oss</sub>	_	10.6	_		- 10	Fig. 16	
Turn-On Switching Energy (Body Diode FWD)	Eon	_	176	_	μJ	$V_{DS} = 400 \text{ V}, V_{GS} = -4 \text{ V}/15 \text{ V}, I_{D} = 15 \text{ A},$	Fig.	
Turn Off Switching Energy (Body Diode FWD)	E <sub>off</sub>	_	36	_		$R_{G(ext)} = 2.5 \Omega, L= 99 \mu H, T_J = 150^{\circ} C$	26, 29	
Turn-On Delay Time	t <sub>d(on)</sub>	_	6	_		$V_{DD} = 400 \text{ V}, V_{GS} = -4 \text{ V}/15 \text{ V}$		
Rise Time	t <sub>r</sub>	_	32	_		$I_D = 15 \text{ A}, R_{G(ext)} = 2.5 \Omega,$	Fig. 27, 29	
Turn-Off Delay Time	t <sub>d(off)</sub>	_	14	_	ns	Timing relative to V <sub>DS</sub>		
Fall Time	t <sub>f</sub>	_	7	_		Inductive load		
Internal Gate Resistance	R <sub>G(int)</sub>	_	13	_	Ω	$f = 1 \text{ MHz}, V_{AC} = 25 \text{ mV}$		
Gate to Source Charge	$Q_{\rm gs}$	_	5	_		$V_{DS} = 400 \text{ V}, V_{GS} = -4 \text{ V}/15 \text{ V}$		
Gate to Drain Charge	$Q_{\mathrm{gd}}$	_	8	_	nC	I <sub>D</sub> = 15 A	Fig. 12	
Total Gate Charge	Qg	_	21	_		Per IEC60747-8-4 pg 21		

## **Reverse Diode Characteristics** ( $T_c = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Тур.	Max.	Unit	<b>Test Conditions</b>	Note
Diode Forward Voltage	V	4.8	_	V	$V_{GS} = -4 \text{ V}, I_{SD} = 7.5 \text{ A}$	Fig.
	V <sub>SD</sub>	4.4	_		$V_{GS} = -4 \text{ V}, I_{SD} = 7.5 \text{ A}, T_{J} = 150^{\circ}\text{C}$	8, 9, 10
Continuous Diode Forward Current <sup>1</sup>	Is	_	15	_	V = 4V	Note 1
Diode Pulse Current <sup>1</sup>	I <sub>S, pulsed</sub>	_	50	A	$V_{GS} = -4 V$	Note 1
Reverse Recover Time <sup>1</sup>	t <sub>rr</sub>	28	_	nS	$V_{GS} = -4 \text{ V}$ , pulse width $t_P$ limited by $T_{j \text{ max}}$	Note 1
Reverse Recovery Charge <sup>1</sup>	Qrr	127	_	nC	$V_{GS} = -4 \text{ V}, I_{SD} = 20 \text{ A}, V_{R} = 400 \text{ V}$	Note 1
Peak Reverse Recovery Current <sup>1</sup>	I <sub>rrm</sub>	6	_	Α	$dif/dt = 600 \text{ A/}\mu\text{s}, T_J = 150^{\circ}\text{C}$	Note 1

## **Thermal Characteristics**

Parameter	Symbol	Max	Unit	Note
Thermal Resistance from Junction to Case	$R_{\theta JC}$	1.3	9 <i>C</i> //M	Fi- 21
Thermal Resistance From Junction to Ambient	$R_{\theta JA}$	40	°C/W	Fig. 21

Note:

<sup>&</sup>lt;sup>3</sup>Turn-off and Turn-on switching energy and timing values measured using SiC MOSFET Body Diode

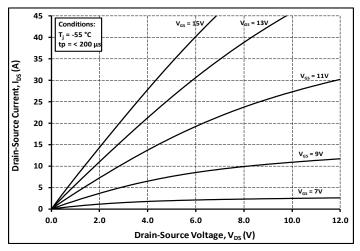


Figure 1. Output Characteristics T<sub>J</sub> = -55°C

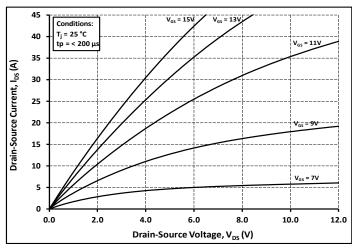


Figure 2. Output Characteristics T<sub>J</sub> = 25°C

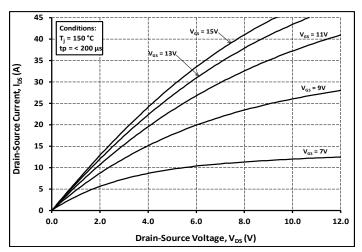


Figure 3. Output Characteristics T<sub>J</sub> = 150°C

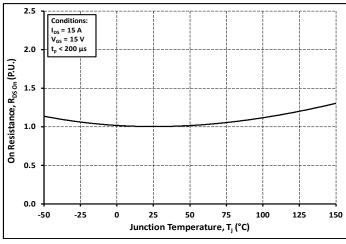
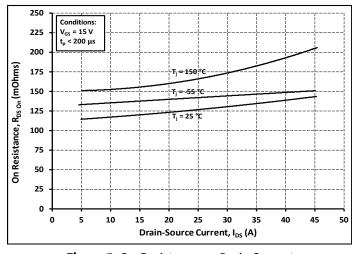
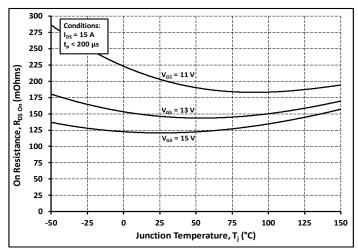


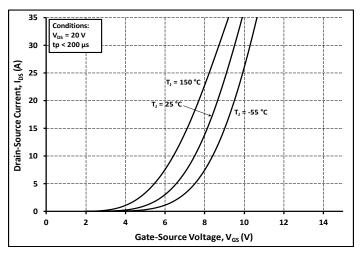
Figure 4. Normalized On-Resistance vs. Temperature



**Figure 5.** On-Resistance vs. Drain Current For Various Temperatures



**Figure 6.** On-Resistance vs. Temperature For Various Gate Voltage



**Figure 7.** Transfer Characteristic for Various Junction Temperatures

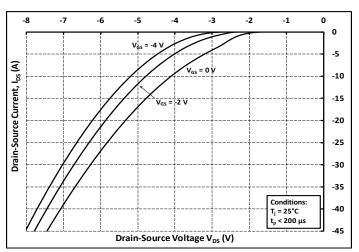


Figure 9. Body Diode Characteristic at 25°C

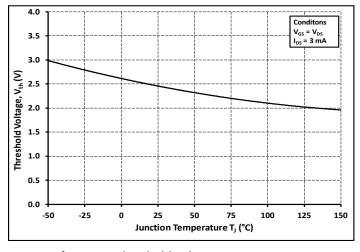


Figure 11. Threshold Voltage vs. Temperature

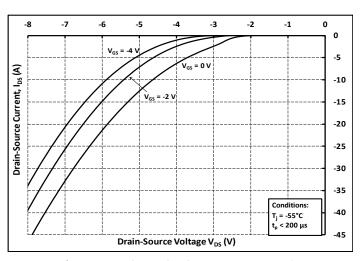


Figure 8. Body Diode Characteristic at -55°C

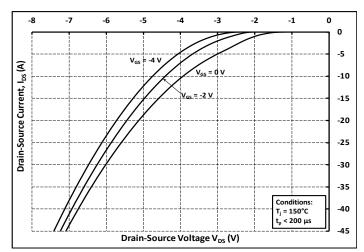


Figure 10. Body Diode Characteristic at 150°C

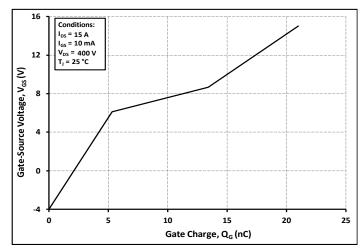


Figure 12. Gate Charge Characteristics

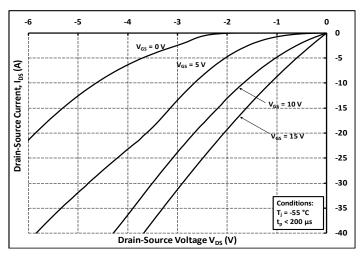


Figure 13. 3rd Quadrant Characteristic at -55°C

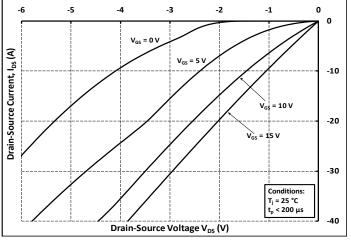


Figure 14. 3rd Quadrant Characteristic at 25°C

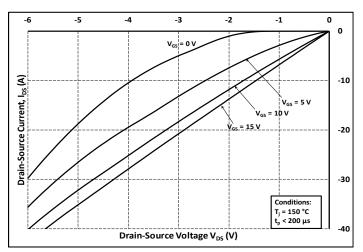


Figure 15. 3rd Quadrant Characteristic at 150°C

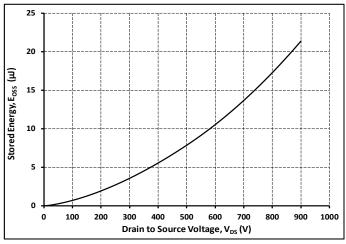
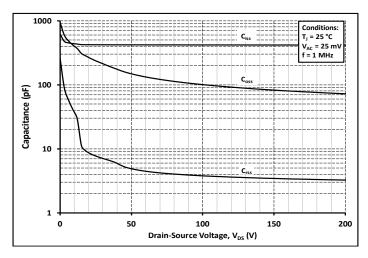
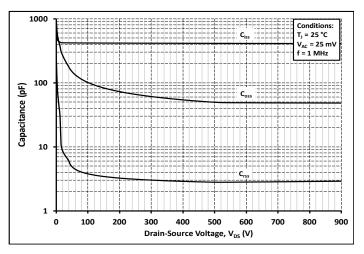


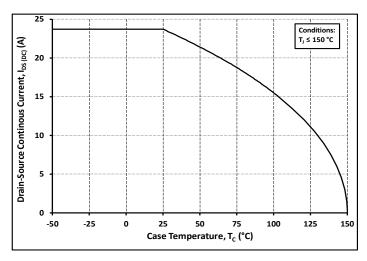
Figure 16. Output Capacitor Stored Energy



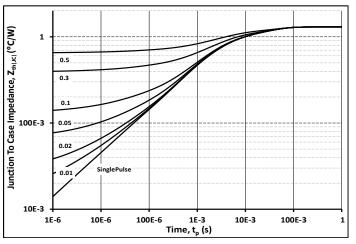
**Figure 17.** Capacitances vs. Drain-Source Voltage (0 - 200 V)



**Figure 18.** Capacitances vs. Drain-Source Voltage (0 - 900 V)



**Figure 19.** Continuous Drain Current Derating vs. Case Temperature



**Figure 21.** Transient Thermal Impedance (Junction - Case)

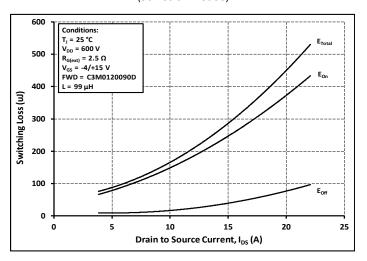
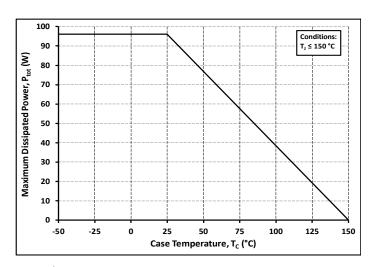


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ( $V_{DD} = 600 \text{ V}$ )



**Figure 20.** Maximum Power Dissipation Derating vs. Case Temperature

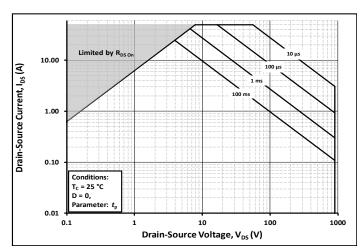
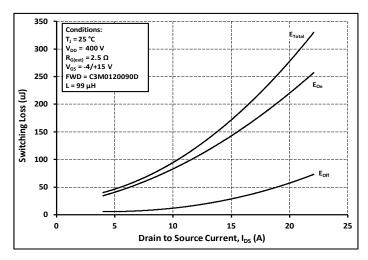


Figure 22. Safe Operating Area



**Figure 24.** Clamped Inductive Switching Energy vs. Drain Current  $(V_{DD} = 400 \text{ V})$ 

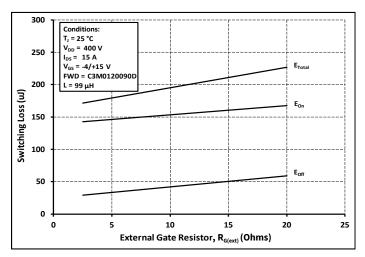


Figure 25. Clamped Inductive Switching Energy vs R<sub>G(ext)</sub>

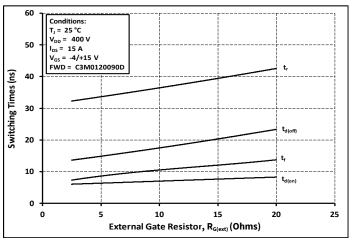


Figure 27. Switching Times vs. R<sub>G(ext)</sub>

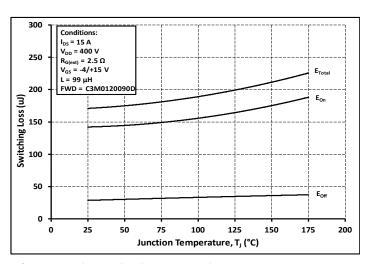


Figure 26. Clamped Inductive Switching Energy vs Temperature

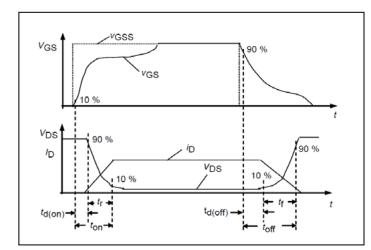


Figure 28. Switching Times Definition

## **Test Circuit Schematic**

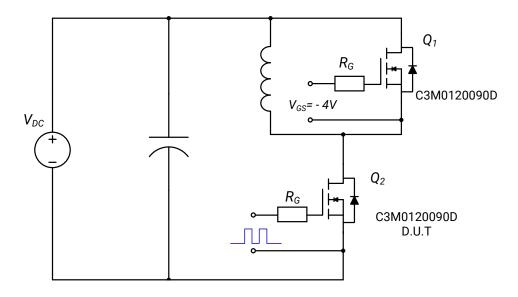
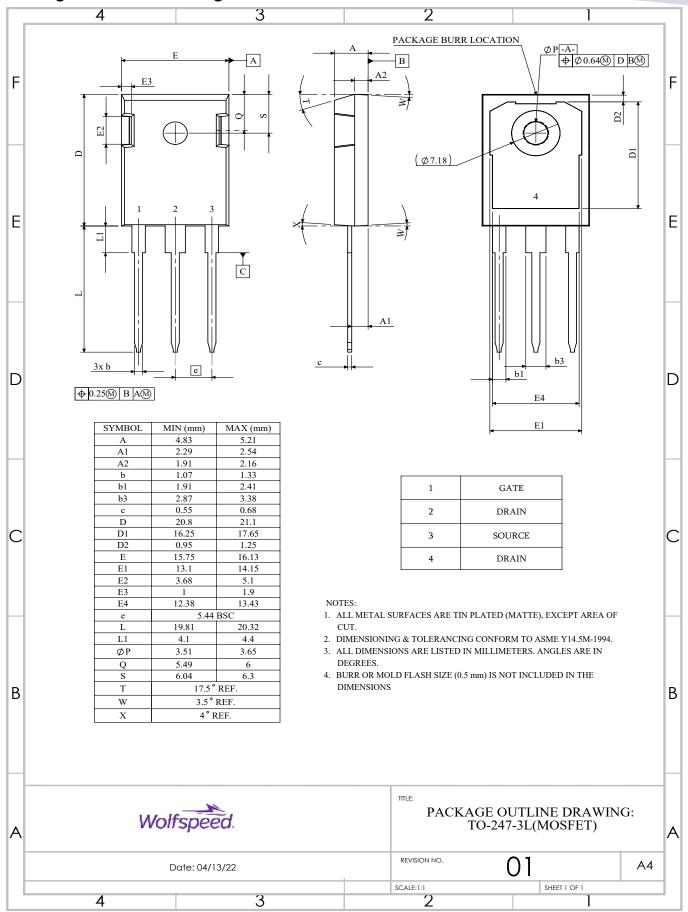


Figure 29. Clamped Inductive Switching Test Circuit

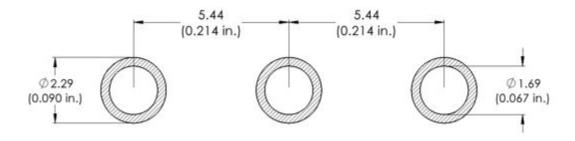
#### Note:

Turn-off and Turn-on switching energy and timing values measured using SiC MOSFET Body Diode as shown above.

## Package Dimensions - Package TO-247-3



## **Recommended Solder Pad Layout**



## **Revision History**

<b>Current Revision</b>	Date of Release	Description of Changes
2	October-2020	N/A
3	November-2023	Not Released
4	December-2023	Updated Wolfspeed branding, package drawing, package image, and solder pad layout, added Revision History Table, Table 1 layout revised

### **Related Links**

- SPICE Models
- SiC MOSFET Isolated Gate Driver reference design
- SiC MOSFET Evaluation Board

#### Notes & Disclaimer

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The Silicon Carbide MOSFET module switches at speeds beyond what is customarily associated with IGBT-based modules. Therefore, special precautions are required to realize optimal performance. The interconnection between the gate driver and module housing needs to be as short as possible. This will afford optimal switching time and avoid the potential for device oscillation. Also, great care is required to insure minimum inductance between the module and DC link capacitors to avoid excessive VDS overshoot.

#### **RoHS Compliance**

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Documentation sections of www.wolfspeed.com.

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