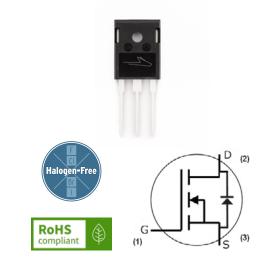


Silicon Carbide Power MOSFET C3M™ MOSFET Technology N-Channel Enhancement Mode

Features

- C3M[™] SiC MOSFET technology
- High blocking voltage with low On-resistance
- High speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery (Q_r)
- Halogen free, RoHS compliant



Ordering Part Number	Package	Marking	$\mathbf{T}_{_{\mathrm{J}}}$, $\mathbf{T}_{_{\mathrm{stg}}}$ Range	
C3M0075120D	TO 247-3	C3M0075120D	-55 - 150 °C	

Applications

- Renewable energy
- EV battery chargers
- High voltage DC/DC converters
- Switch Mode Power Supplies

Benefits

- Higher system efficiency
- Reduced cooling requirements
- Increased power density
- Increased system switching frequency

Key Parameters

Parameter	Symbol	Min.	Тур.	Max	Unit	Conditions	Note
Drain - Source Voltage	V _{DS}			1200	V	T _c = 25°C	
Maximum Gate - Source Voltage	V _{GS(max)}	-8		+19	\ \	Transient	
Operational Gate-Source Voltage	V _{GS op}		-4/15			Static	Note 1
DC Continuous Drain Current	I _D			32		$V_{GS} = 15 \text{ V}, T_{C} = 25 \text{ °C}, T_{J} \le 150 \text{ °C}$	Fig. 19 Note 2
				23	A	V _{GS} = 15 V, T _C = 100 °C, T _J ≤150 °C	
Pulsed Drain Current	I _{DM}			123		t _{Pmax} limited by T _{jmax} V _{GS} = 15V, T _C = 25 °C	Fig. 22
Power Dissipation	P _D			136	W	$T_{c} = 25^{\circ}C, T_{J} = 150^{\circ}C$	Fig. 20
Operating Junction and Storage Temperature	T_{J},T_{stg}			-55 to +150	°c		
Solder Temperature	T _L			260		According to JEDEC J-STD-020	
Mounting Torque	M _D			1 8.8	Nm Ibf-in	M3 or 6-32 screw	

Note (1): Recommended turn-on gate voltage is 15V with ±5% regulation tolerance, see Application Note PRD-04814 for additional details

Note (2): Verified by design

Electrical Characteristics ($T_c = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Test Conditions	Note
Drain-Source Breakdown Voltage	V _{(BR)DSS}	1200	_	_		$V_{GS} = 0 \text{ V}, I_{D} = 100 \mu\text{A}$	
C. T. L. IV.	.,	1.8	2.5	3.6	V	$V_{DS} = V_{GS}$, $I_D = 5 \text{ mA}$	Fig. 11
Gate Threshold Voltage	$V_{GS(th)}$	_	2.2	_		$V_{DS} = V_{GS}$, $I_D = 5$ mA, $T_J = 150$ °C	
Zero Gate Voltage Drain Current	I _{DSS}	_	1	100	μΑ	V _{DS} = 1200 V, V _{GS} = 0 V	
Gate-Source Leakage Current	I _{GSS}	_	10	250	μΑ	$V_{GS} = 15 \text{ V}, V_{DS} = 0 \text{ V}$	
Drain-Source On-State Resistance		_	75	90	mΩ	$V_{GS} = 15 \text{ V}, I_D = 20 \text{ A}$	Fig. 4, 5, 6
Diam-Source On-State Resistance	R _{DS(on)}	_	120	_	11122	$V_{GS} = 15 \text{ V}, I_D = 20 \text{ A}, T_J = 150^{\circ}\text{C}$	
Transconductance	σ.		12		S	$V_{DS} = 20 \text{ V}, I_{DS} = 20 \text{ A}$	Fig. 7
	g _{fs}	_	13	_	3	$V_{DS} = 20 \text{ V}, I_{DS} = 20 \text{ A}, T_{J} = 150^{\circ}\text{C}$	
Input Capacitance	C _{iss}	_	1390	_			Fig. 17, 18
Output Capacitance	C _{oss}	_	58	_	pF	$V_{GS} = 0 \text{ V}, V_{DS} = 1000 \text{ V}$ f = 1 Mhz $V_{AC} = 25 \text{ mV}$	
Reverse Transfer Capacitance	C _{rss}	_	2	_			
C _{oss} Stored Energy	E _{oss}	_	33	_			Fig. 16
Turn-On Switching Energy (SiC Diode FWD)	Eon	_	564	_		$V_{DS} = 800 \text{ V}, V_{GS} = -4 \text{ V}/15 \text{ V}, I_{D} = 20 \text{ A},$ $R_{G(ext)} = 0 \Omega, L = 157 \mu\text{H}, T_{J} = 150 ^{\circ}\text{C}$	Fig. 26, 29
Turn Off Switching Energy (SiC Diode FWD)	E _{off}	_	186	_	μJ		
Turn-On Switching Energy (Body Diode FWD)	Eon	_	924	_			
Turn Off Switching Energy (Body Diode FWD)	E _{off}	_	162	_			
Turn-On Delay Time	t _{d(on)}	_	56	_			Fig. 27, 28
Rise Time	t _r	_	17	_		$\begin{split} V_{DD} &= 800 \text{ V, } V_{GS} = -4 \text{ V/15 V} \\ I_D &= 20 \text{ A, } R_{G(ext)} = 0 \Omega \\ \text{Timing relative to V}_{DS} \\ \text{Inductive load} \end{split}$	
Turn-Off Delay Time	t _{d(off)}	_	32	_	ns		
Fall Time	t _f	_	13	_			
Internal Gate Resistance	R _{G(int)}	_	9.0	_	Ω	f = 1 MHz, V _{AC} = 25 mV	
Gate to Source Charge	$Q_{\rm gs}$	_	17	_		V _{DS} = 800 V, V _{GS} = -4 V/15 V	Fig. 12
Gate to Drain Charge	Q_{gd}	_	20	_	nC	I _D = 20 A	
Total Gate Charge	Qg	_	54	_		Per IEC60747-8-4 pg 21	

Reverse Diode Characteristics ($T_c = 25^{\circ}C$ unless otherwise specified)

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Diode Forward Voltage	V_{SD}	4.5	_	V	$V_{GS} = -4 \text{ V}, I_{SD} = 10 \text{ A}$	Fig. 8, 9, 10
		4.0	_		$V_{GS} = -4 \text{ V}, I_{SD} = 10 \text{ A}, T_J = 150^{\circ}\text{C}$	
Continuous Diode Forward Current ¹	Is	_	26		V _{GS} = -4 V, T _J = 25°C	
Diode Pulse Current¹	I _{SM}	_	123	1	$V_{GS} = -4 \text{ V}$, pulse width t_P limited by $T_{j \text{ max}}$	
Reverse Recovery Time ¹	t _{rr}	48	_	ns	$V_{GS} = -4 \text{ V}, I_{SD} = 20 \text{ A}, V_{R} = 800 \text{ V}$ $di_{p}/dt = 2800 \text{ A}/\mu\text{s}$	
Reverse Recovery Charge ¹	Qrr	279	_	nC		
Peak Reverse Recovery Current ¹	I _{RRM}	9	_	А	T _J = 150°C	

Note

Thermal Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Thermal Resistance from Junction to Case	$R_{\theta JC}$	0.97	1.1	°C /\\\		Fig. 21
Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	_	40	°C/W		Fig. 21

 $^{^{\}rm 1}$ When using MOSFET Body Diode V $_{\rm GS\,max}$ = -4V/+19V

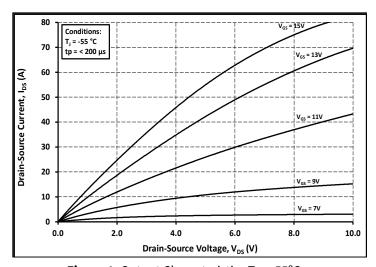


Figure 1. Output Characteristics T_J = -55°C

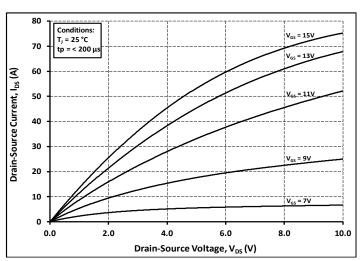


Figure 2. Output Characteristics T_J = 25°C

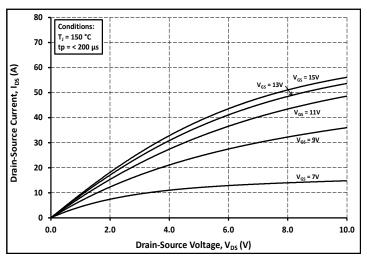


Figure 3. Output Characteristics T_J = 150°C

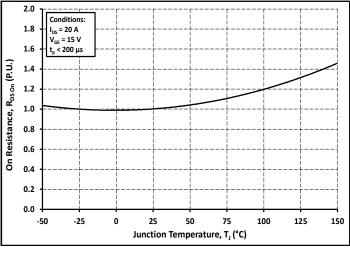


Figure 4. Normalized On-Resistance vs. Temperature

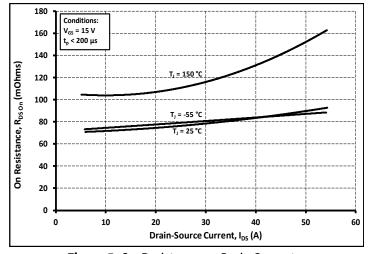


Figure 5. On-Resistance vs. Drain Current For Various Temperatures

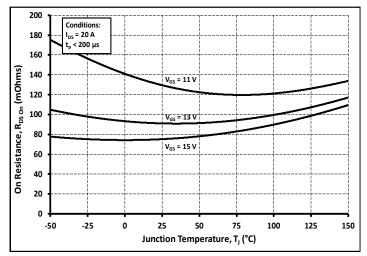


Figure 6. On-Resistance vs. Temperature For Various Gate Voltage

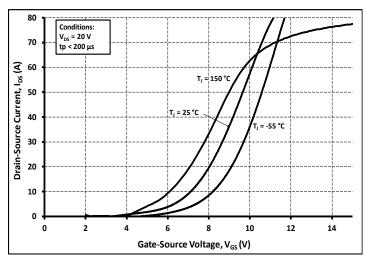


Figure 7. Transfer Characteristic for Various Junction Temperatures

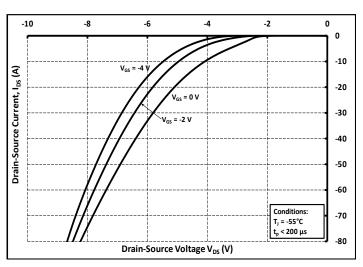


Figure 8. Body Diode Characteristic at -55°C

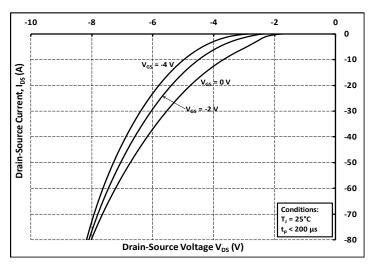


Figure 9. Body Diode Characteristic at 25°C

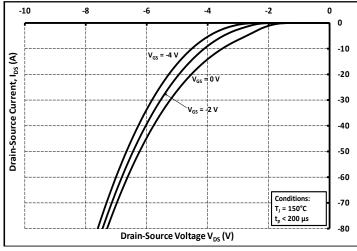


Figure 10. Body Diode Characteristic at 150°C

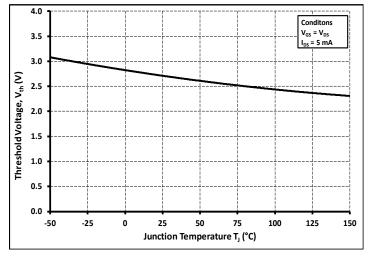


Figure 11. Threshold Voltage vs. Temperature

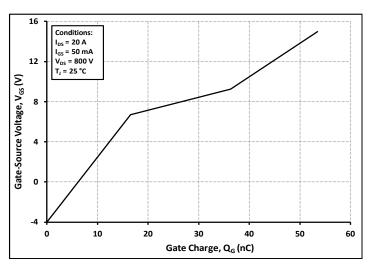


Figure 12. Gate Charge Characteristics

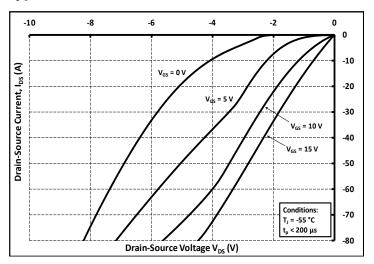


Figure 13. 3rd Quadrant Characteristic at -55°C

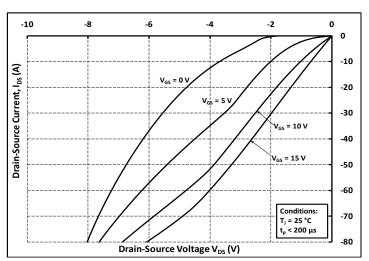


Figure 14. 3rd Quadrant Characteristic at 25°C

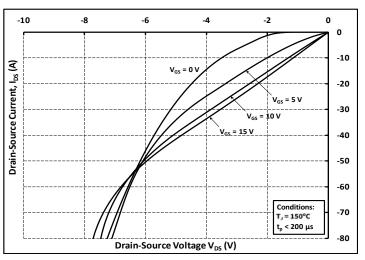


Figure 15. 3rd Quadrant Characteristic at 150°C

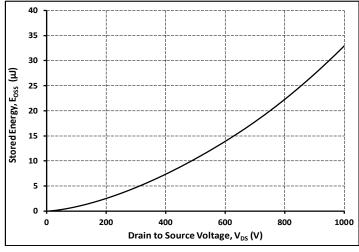


Figure 16. Output Capacitor Stored Energy

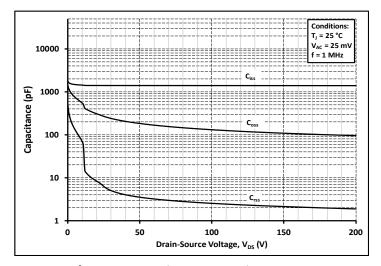


Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

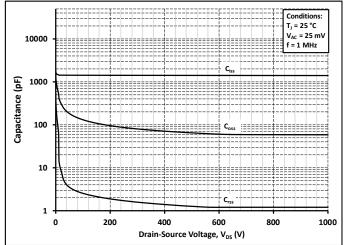


Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1000V)

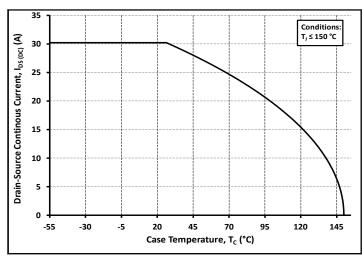


Figure 19. Continuous Drain Current Derating vs. Case Temperature

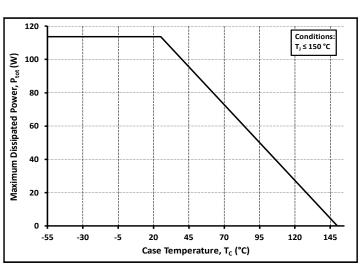


Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

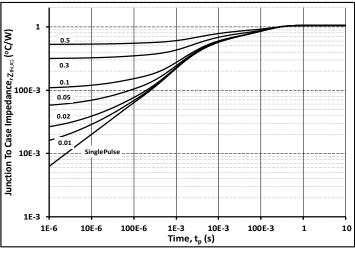


Figure 21. Transient Thermal Impedance (Junction - Case)

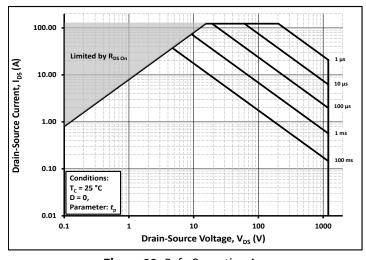


Figure 22. Safe Operating Area

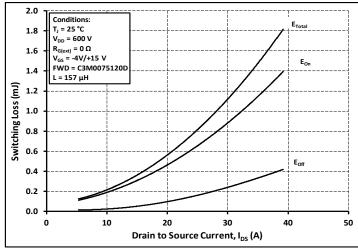


Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD} = 600 \text{ V}$)

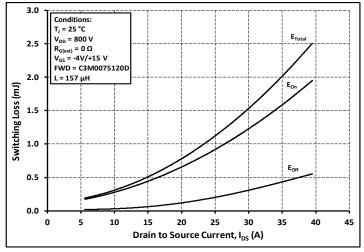


Figure 24. Clamped Inductive Switching Energy vs. Drain Current $(V_{DD} = 800 \text{ V})$

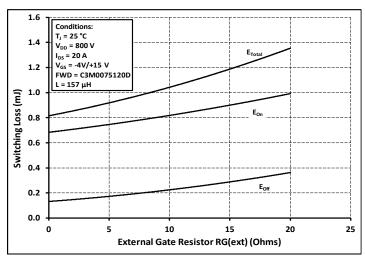


Figure 25. Clamped Inductive Switching Energy vs. R_{G(ext)}

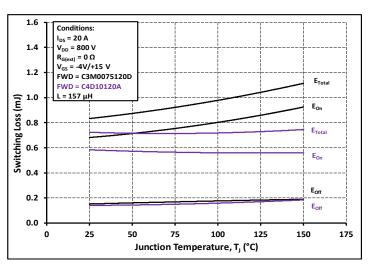


Figure 26. Clamped Inductive Switching Energy vs. Temperature

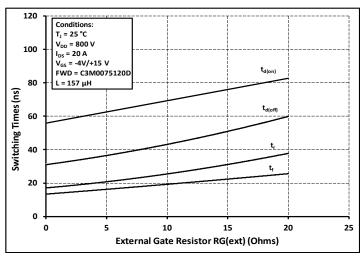


Figure 27. Switching Times vs. R_{G(ext)}

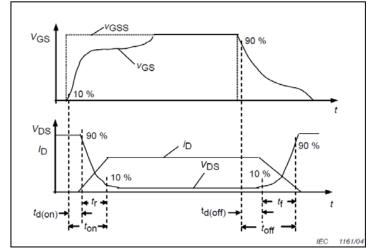


Figure 28. Switching Times Definition

Test Circuit Schematic

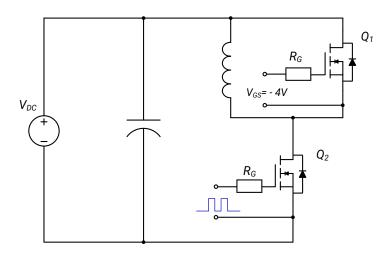
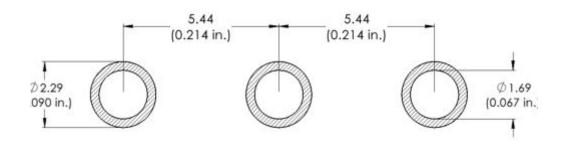


Figure 29. Clamped Inductive Switching Waveform Test Circuit

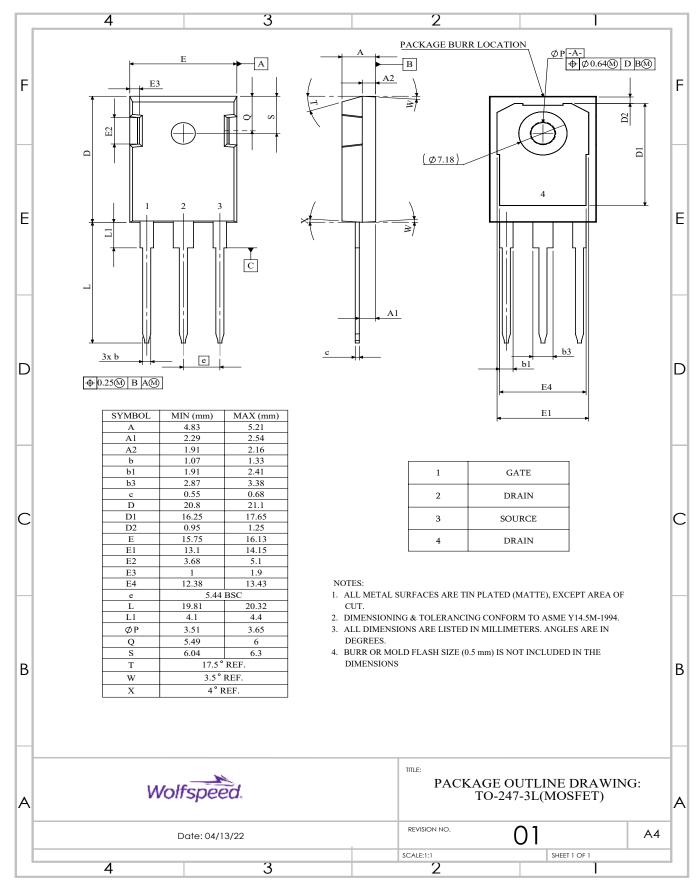
Note:

Turn-off and Turn-on switching energy and timing values measured using SiC MOSFET Body Diode as shown above.

Recommended Solder Pad Layout



Package Dimensions – Package TO-247-3



Revision History

Revision	Revision. Date	Comments
Rev 3	January 2021	N/A
Rev 5	December-2023	Wolfspeed Branding, POD, Package Image, Solder Pad Layout,added Rev History, Seperate from -A datasheet, Table 1 layout revised

Notes & Disclaimer

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The Silicon Carbide MOSFET module switches at speeds beyond what is customarily associated with IGBT-based modules. Therefore, special precautions are required to realize optimal performance. The interconnection between the gate driver and module housing needs to be as short as possible. This will afford optimal switching time and avoid the potential for device oscillation. Also, great care is required to insure minimum inductance between the module and DC link capacitors to avoid excessive VDS overshoot.

RoHS Compliance

The levels of RoHS restricted materials in this product are below the maximum concentration values (also referred to as the threshold limits) permitted for such substances, or are used in an exempted application, in accordance with EU Directive 2011/65/EC (RoHS2), as implemented January 2, 2013. RoHS Declarations for this product can be obtained from your Wolfspeed representative or from the Product Documentation sections of www.wolfspeed.com.

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