

4th Generation 1200 V, 40 A Silicon Carbide Schottky Diode

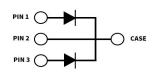
Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.









Package Types: TO-247-3 Marking: C4D40120

Features

- High-Frequency Operation
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Parallel Devices Without Thermal Runaway

Applications

- Boost Diodes in PFC or DC/DC Stages
- Free Wheeling Diodes in Inverter Stages
- Switch Mode Power Supplies
- Solar Inverters
- AC/DC Converters

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

* Per Leg, ** Per Device

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	1200				
Surge Peak Reverse Voltage	V _{RSM}	1300	V			
DC Blocking Voltage	V _{DC}	1200				
Continuous Forward Current (Per Leg/Per Device)		56.5/113		T _c = 25 °C		
	I _F	27/54		T _c = 135 °C	Fig. 3	
		20/40		T _c = 150 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	91*	А	$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
		61*		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
Non-Repetitive Forward Surge Current	I _{FSM}	130*		$T_c = 25 ^{\circ}\text{C}, t_D = 10 \text{ms}, \text{Half Sine Wave}$		
		110*		$T_c = 110 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	Fig. 8	
Non-Repetitive Peak Forward Surge Current	I _{F,Max}	1150*		$T_c = 25 ^{\circ}\text{C}, t_D = 10 \mu\text{s}, \text{Pulse}$		
		950*		$T_{c} = 110 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$		
Power Dissipation (Per Leg/Per Device)	P _{tot}	266/532	W	T _c = 25 °C		
		114/228		T _C = 110 °C	Fig. 4	
i²t value	ʃi²dt	84.5*	A ² s	T _c = 25C, tp=10ms		
		60.5*		T _c = 110C, tp=10ms		
Diode dV/dt Ruggedness	dV/dt	200	V/ns	V _R = 0-960V		

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Forward Voltage	V	1.5	1.8	V	I _F = 20 A, T _j = 25 °C	Fig. 1
	V _F	2.2	3		I _F = 20 A, T _j = 175 °C	
Reverse Current		35	200		V _R = 1200 V, T _j = 25 °C	Fig. 2
	I _R	65	400	μΑ	V _R = 1200 V, T _j = 175 °C	Fig. 2
Total Capacitive Charge	Q _c	99		nC	$V_R = 800 \text{ V}, T_j = 25 ^{\circ}\text{C}$ $I_F = 20A, \text{ di/dt} = 200A/\mu\text{s}$	Fig. 5
		1500			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	C	93		pF	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		67			$V_R = 800 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	28		μJ	V _R = 800 V	Fig. 7

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{e, JC (TYP)}	0.29** 0.57*	°C/W	
Junction Temperature	T _j	-55 to +175	°C	
Storage Temperature	T _{stg}	-55 to +135	°C	
		1	Nm	M3 Screw
TO-247 Mounting Torque	-	8.8	lbf-in	6-32 Screw

^{*} Per Leg, ** Per Device

Typical Performance

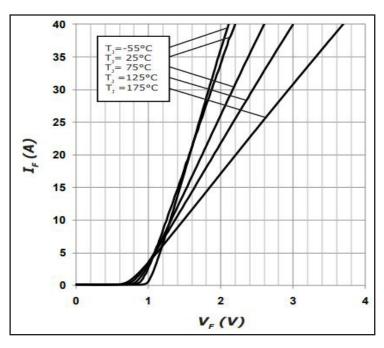


Figure 1Forward Characteristics

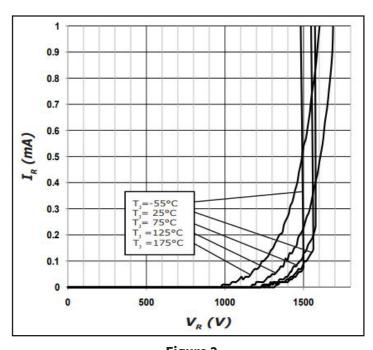


Figure 2Reverse Characteristics

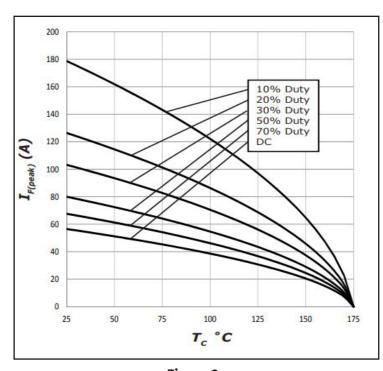


Figure 3Current Derating

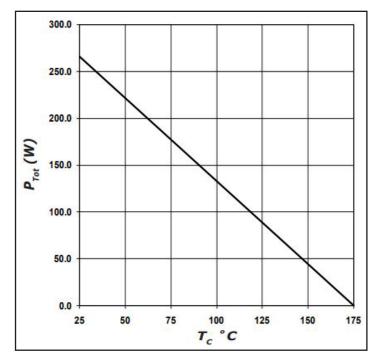
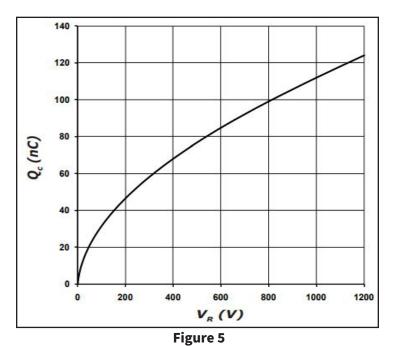


Figure 4Power Derating

Typical Performance



Total Capacitance Charge vs. Reverse Voltage

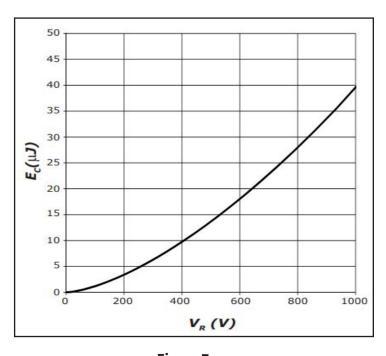
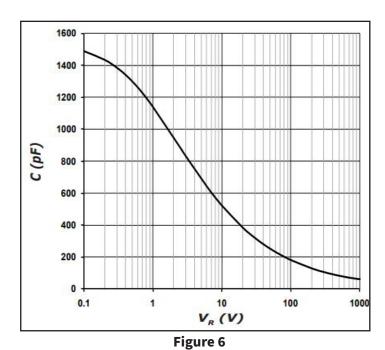


Figure 7Capacitance Stored Energy



Capacitance vs. Reverse Voltage

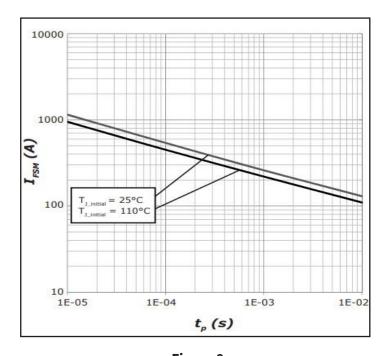


Figure 8Non-Repetitive Peak Forward Surge Current vs. Pulse Duration

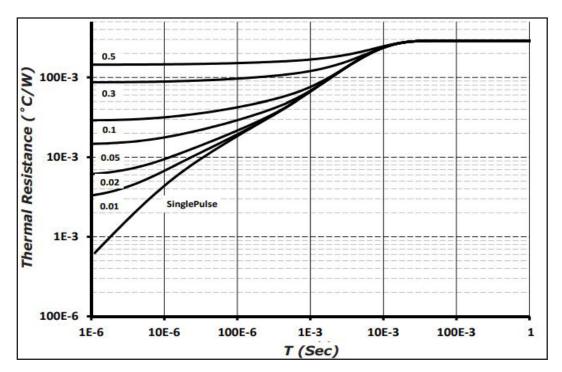
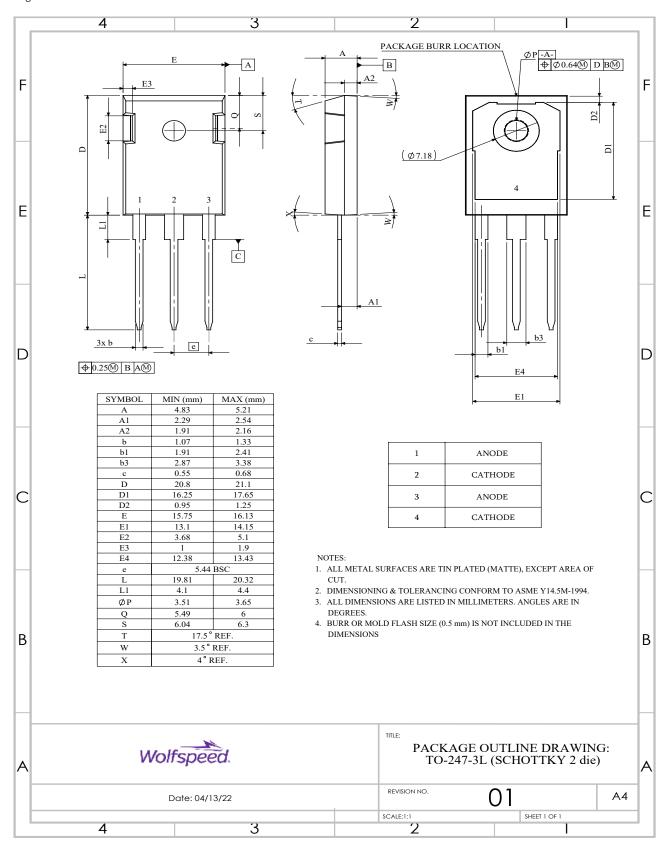


Figure 9Transient Thermal Impedance

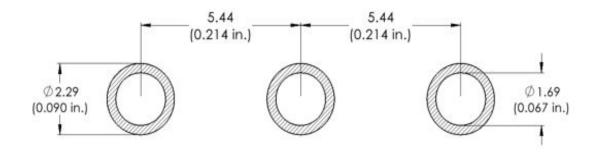
Package Dimensions & Pin-Out

Package: TO-247-3



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Diode Model

$$\begin{array}{c|c} - & & \\ \hline V_T & & \\ \hline R_T \end{array}$$

$$V_{fT} = V_T + If^*R_T$$

$$V_T = 0.97 + (T_J^* -1.40^*10^{-3})$$

$$R_T = 0.023 + (T_J^* 2.71^*10^{-4})$$

Note: T_j = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

Product Ordering Information

Order Number	Packing Type
C4D40120D	Tube

 $\label{lem:REACh} \textbf{REACh}, \textbf{RoHS}, \textbf{and Halogen-Free compliance documentation available for this product}.$

Revision History

Document Version	Date of Release	Description of Changes
G	September- 2016	Initial Release
8	November-2023	Update Branding, POD, Package Image, Solder pad layout

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