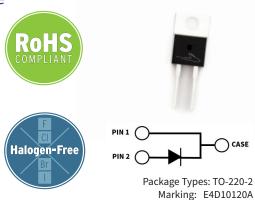


E-Series Automotive 4th Generation 1200 V, 10 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Automotive Qualified (AEC Q101) and PPAP Capable

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	1200				
DC Blocking Voltage	V _{DC}	1200	V			
		33		T _J = 25 °C		
Continuous Forward Current	I _F	16		T _J = 135 °C	Fig. 3	
		10	Α	T _J = 156 °C		
Repetitive Peak Forward Surge		44		$T_c = 25 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave		
Current	FRM	26		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
		166		T _J = 25 °C		
Power Dissipation	P _{tot}	72	W	T _J = 110 °C	Fig. 4	
Diode dV/dt ruggedness	dV/dt	250	V/ns	V _R = 0-960V		

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
Famous ad Malhama		1.5	1.8		I _F = 10 A, T _j = 25 °C	F:_ 1
Forward Voltage	V _F	2.2		V	I _F = 10 A, T _j = 175 °C	Fig. 1
Reverse Current		30	200	μΑ	$V_R = 1200 \text{ V}, T_j = 25 \text{ °C}$	Fig. 2
	I _R	55			V _R = 1200 V, T _j = 175 °C	
Total Capacitive Charge	Q _c	56		nC	$V_R = 800 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		77			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	51		pF	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		44			$V_R = 800 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	17		μJ	V _R = 800 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	0.9	°C/W	
Junction Temperature	T _j	-55 to +175	°C	
Case & Storage Temperature	T _c	-55 to +175		
TO 000 W		1	Nm	M3 Screw
TO-220 Mounting Torque	-	8.8	lbf-in	6-32 Screw

Electrostatic Discharge (ESD) Classifications

Parameter	Symbol	Notes
Human Body Model	НВМ	Class 3B (≥ 8000 V)
Charge Device Model	CDM	Class C3 (≥ 1000 V)

Typical Performance

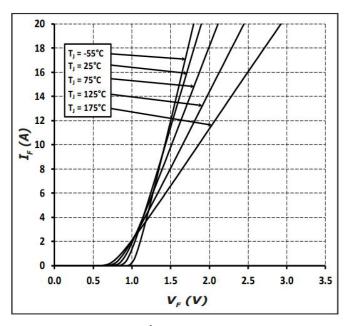


Figure 1Forward Characteristics

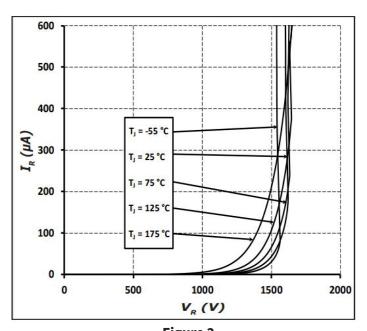


Figure 2Reverse Characteristics

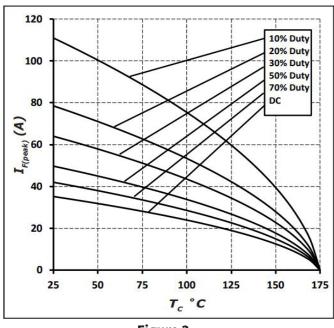


Figure 3Current Derating

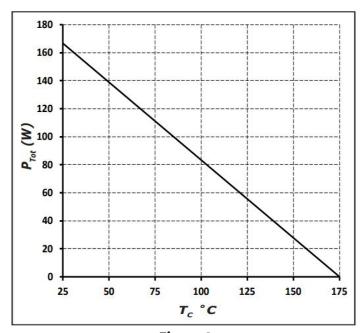


Figure 4Power Derating

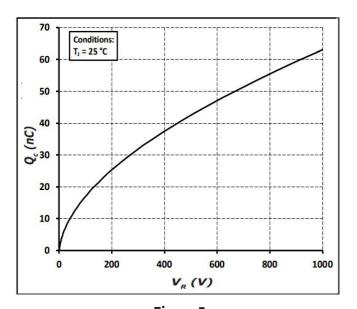
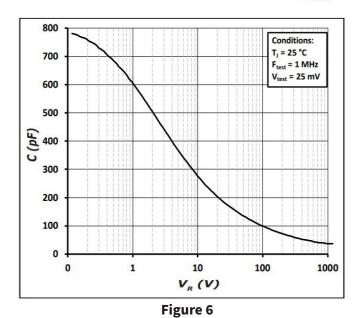


Figure 5Total Capacitance vs. Reverse Voltage



Capacitace vs. Reverse Voltage

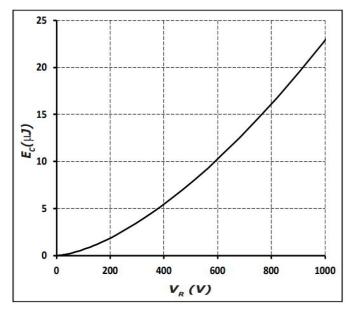


Figure 7Capacitance Stored Energy

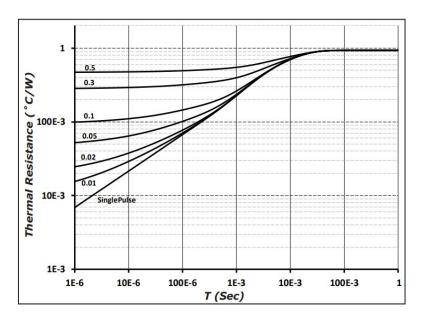


Figure 9Transient Thermal Impedance

Diode Model

$$V_{fT} = V_T + If^*R_T$$

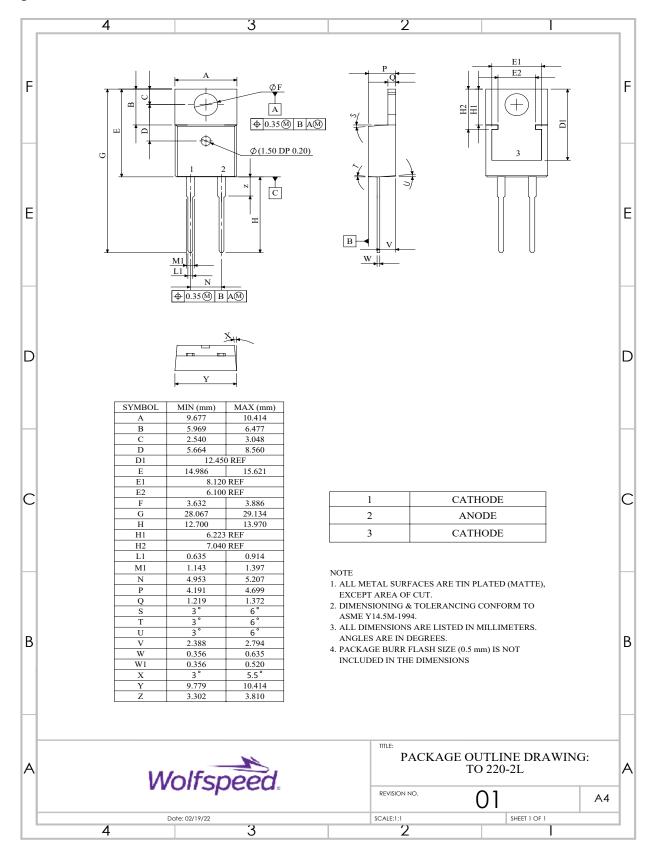
$$V_T = 1.00 + (T_J^* - 1.10^*10^{-3})$$

$$R_T = 0.03 + (T_J^* 4.00^*10^{-4})$$

Note: T_J = Diode Junction Temperature In Degrees Celsius, valid from 25°C to 175°C

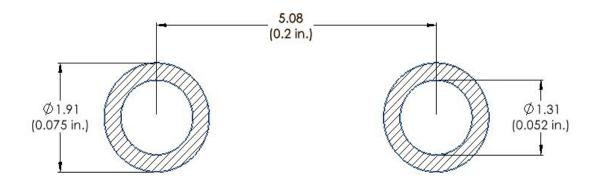
Package Dimensions & Pin-Out

Package: TO-220-2



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type		
E4D10120A	Tube		

Revision History

Document Version	Date of Release	Description of Changes
0	July-2016	Initial Release
1	March-2023	Update Package Drawing Update Landing Pad

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