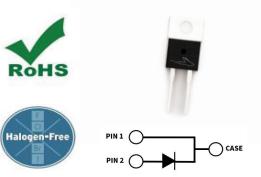


4th Generation 1200 V, 20 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Package Types: TO-220-2 Marking: C4D20120A

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings (T_c = 25°C Unless Otherwise Specified)

| Parameter | Symbol | Value | Unit | Test Conditions | Notes | |
|---|--------------------|-------|------------------|---|--------|--|
| Repetitive Peak Reverse Voltage | V _{RRM} | 1200 | | | | |
| DC Blocking Voltage | V _{DC} | 1200 | V | | | |
| | | 54.5 | | T _J = 25 °C | | |
| Continuous Forward Current | I _F | 26 | | T _J = 135 °C | Fig. 3 | |
| | | 20 | | T _J = 150 °C | | |
| Repetitive Peak Forward Surge | | 91 | | T _c = 25 °C, t _p = 10 ms, Half Sine Wave | | |
| Current | FRM | 61 | А | $T_{c} = 110 ^{\circ}\text{C}, t_{p} = 10 \text{ms}, Half Sine Wave}$ | | |
| Non-Repetitive Forward Surge Current | I _{FSM} | 130 | | T _c = 25 °C, t _p = 10 ms, Half Sine Wave | Fig. 8 | |
| | | 110 | | $T_{c} = 110 ^{\circ}\text{C,t}_{p} = 10 \text{ms, Half Sine Wave}$ | | |
| Non-Repetitive Peak Forward | l _{F,Max} | 1150 | | $T_{c} = 25 {}^{\circ}\text{C}, t_{p} = 10 \mu\text{s}, \text{Pulse}$ | | |
| Surge Current | | 950 | | T _C = 110°C, t _p = 10 μs, Pulse | | |
| Power Dissipation | P _{tot} | 250 | | T _J = 25 °C | Fig. 4 | |
| | | 112.5 | W | T _J = 110 °C | | |
| i²t Value | ∫i²t | 84.5 | A ² s | $T_{c} = 25 ^{\circ}\text{C}, t_{p} = 10 \text{ms}$ | | |
| | | 60.5 | | $T_{c} = 110^{\circ}C, t_{p} = 10 \text{ ms}$ | | |

Electrical Characteristics

| Parameter | Symbol | Тур. | Max. | Unit | Test Conditions | Notes |
|---------------------------|----------------|------|---|------|---|--------|
| 5 IV II | | 1.5 | 1.8 | | I _F = 20 A, T _j = 25 °C | F. 1 |
| Forward Voltage | V _F | 2.2 | 3 | V | I _F = 20 A, T _j = 175 °C | Fig. 1 |
| Davis and Comment | 35 200 | | $V_R = 1200 \text{ V}, T_j = 25 ^{\circ}\text{C}$ | | | |
| Reverse Current | I _R | 65 | 400 | μΑ | $V_R = 1200 \text{ V}, T_j = 175 \text{ °C}$ | Fig. 2 |
| Total Capacitive Charge | Q_c | 99 | | nC | $V_R = 800 \text{ V}, T_j = 25 \text{ °C}$ | Fig. 5 |
| | | 1500 | | | $V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$ | |
| Total Capacitance | c | 93 | | pF | $V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$ | Fig. 6 |
| | | 67 | | | $V_R = 800 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$ | |
| Capacitance Stored Energy | E _c | 28 | | μJ | V _R = 800 V | Fig. 7 |

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

| Parameter | Symbol | Value | Unit | Notes |
|--|------------------------|-------------|--------|------------|
| Thermal Resistance, Junction to Case (Typical) | $R_{\theta, JC (TYP)}$ | 0.6 | °C/W | |
| Junction Temperature | T _j | -55 to +175 | | |
| Case & Storage Temperature | T _c | -55 to +175 | °C | |
| | | 1 | Nm | M3 Screw |
| TO-220 Mounting Torque | - | 8.8 | lbf-in | 6-32 Screw |

Electrostatic Discharge (ESD) Classifications

| Parameter | Symbol | Notes |
|---------------------|--------|---------------------|
| Human Body Model | НВМ | Class 3B (≥ 8000 V) |
| Charge Device Model | CDM | Class C3 (≥ 1000 V) |

Typical Performance

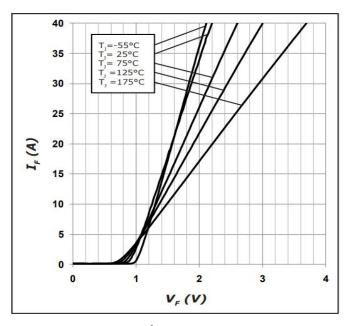


Figure 1Forward Characteristics

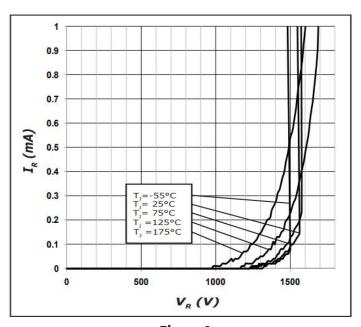


Figure 2Reverse Characteristics

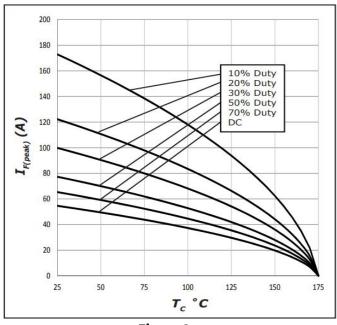


Figure 3Current Derating

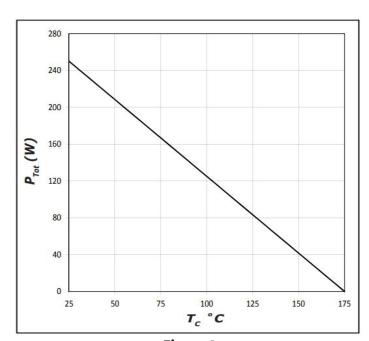


Figure 4Power Derating

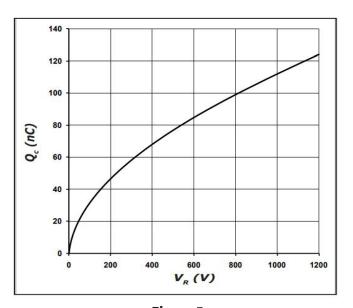


Figure 5Total Capacitance vs. Reverse Voltage

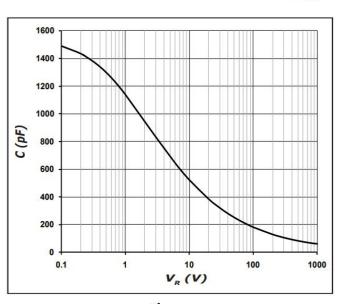


Figure 6Capacitace vs. Reverse Voltage

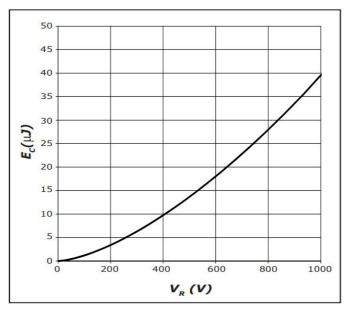


Figure 7Capacitance Stored Energy

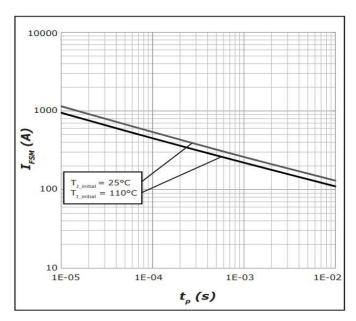


Figure 8Non-Repetitive Peak Forward Surge Current versus Pulse Duration (sinusoidal waveform)

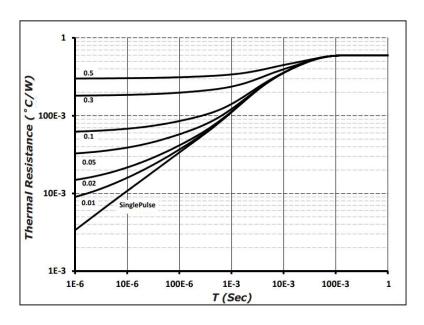
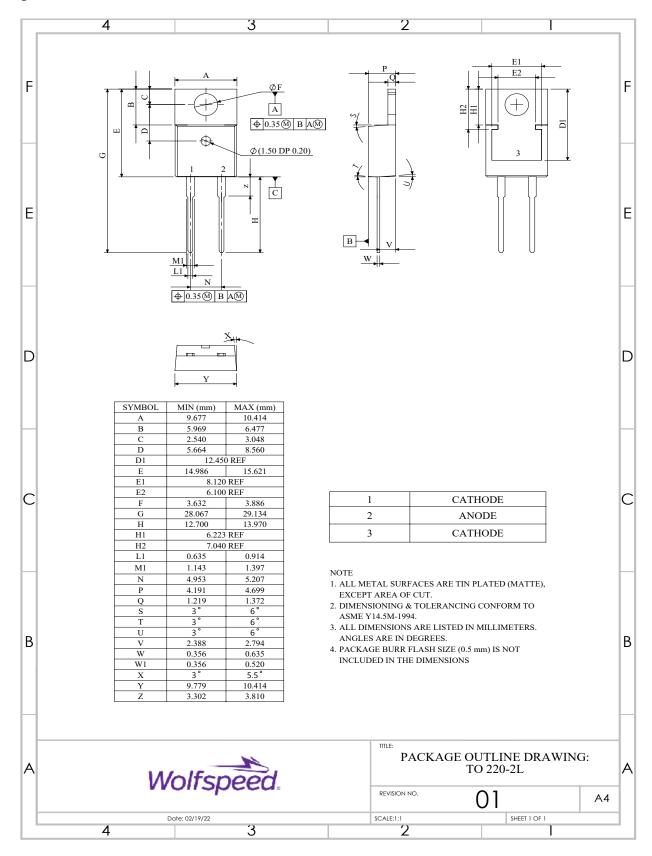


Figure 9Transient Thermal Impedance

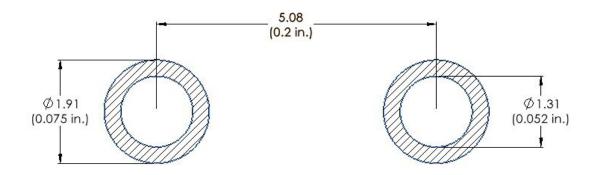
Package Dimensions & Pin-Out

Package: TO-220-2



Recommended Solder Pad Layout

Primary dimensions shown in mm. Learn more about recommended soldering profiles in this application note.



Product Ordering Information

| Order Number | Packing Type | | |
|--------------|--------------|--|--|
| C4D20120A | Tube | | |

Learn more about power device packing & shipment information in this application note.

Revision History

| Document Version | Date of Release | Description of Changes |
|------------------|-----------------|--|
| 1 | October-2016 | Initial Release |
| 6 | March-2023 | Update Package Drawing Update Landing Pad |

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