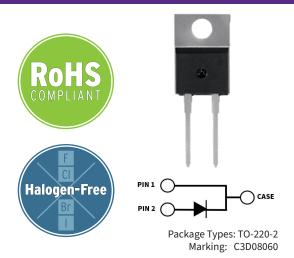


3rd Generation 600V, 8 A Silicon Carbide Schottky

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Wolfspeed, Inc. is in the process of rebranding its products and related materials pursuant to the entity name change from Cree, Inc. to Wolfspeed, Inc. During this transition period, products received may be marked with either the Cree name and/or logo or the Wolfspeed name and/or logo.

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior

Typical Applications

- Industrial Switched Mode Power Supplies
- Uninterruptible & AUX Power Supplies
- Boost for PFC & DC-DC Stages
- Solar Inverters

Maximum Ratings ($T_c = 25^{\circ}$ C Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes	
Repetitive Peak Reverse Voltage	V _{RRM}	600				
DC Blocking Voltage	V _{DC}	600	V			
		24		T _c = 25 °C		
Continuous Forward Current	I _F	11		T _c = 135 °C	Fig. 3	
		8	A	T _c = 152 °C		
Repetitive Peak Forward Surge Current	I _{FRM}	37.5		T _c = 25 °C, t _p = 10 ms, Half Sine Wave		
		25.5		$T_c = 110 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
Non-Repetitive Forward Surge		71		$T_c = 25 ^{\circ}\text{C}, t_p = 10 \text{ms}, \text{Half Sine Wave}$		
Current	FSM	60		$T_c = 110 ^{\circ}\text{C}$, $t_p = 10 \text{ms}$, Half Sine Wave	Fig. 8	
Non-Repetitive Peak Forward		650		T _c = 25 °C, t _p = 10 μs, Pulse		
Surge Current	F,Max	530		$T_{c} = 110 ^{\circ}\text{C}, t_{n} = 10 \mu\text{s}, \text{Pulse}$		
Power Dissipation	P _{tot}	107	W	T _C = 25 °C		
		46.5		T _c = 110 °C	Fig. 4	

Electrical Characteristics

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions	Notes
E IV II		1.5	1.8	.,,	I _F = 8 A, T _j = 25 °C	F: 1
Forward Voltage	V _F	2.1	2.4	V	I _F = 8 A, T _j = 175 °C	Fig. 1
Decrees Comment		8.5	42.5		$V_R = 600 \text{ V}, T_j = 25 \text{ °C}$	F:- 2
Reverse Current	I _R	17	170	μΑ	V _R = 600 V, T _j = 175 °C	Fig. 2
Total Capacitive Charge	Q _c	20		nC	$V_R = 400 \text{ V}, T_j = 25 \text{ °C}$	Fig. 5
		395			$V_R = 0 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Total Capacitance	c	37		pF	$V_R = 200 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	Fig. 6
		32			$V_R = 400 \text{ V}, T_j = 25 \text{ °C}, f = 1 \text{ MHz}$	
Capacitance Stored Energy	E _c	3.0		μJ	V _R = 400 V	Fig. 7

Notes:

SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	R _{0, JC (TYP)}	1.4	°C/W	
Junction Temperature	T _j	-55 to +175		
Case & Storage Temperature	T _c	-55 to +175	°C	
		1	Nm	M3 Screw
TO-220 Mounting Torque	-	8.8	lbf-in	6-32 Screw

Typical Performance

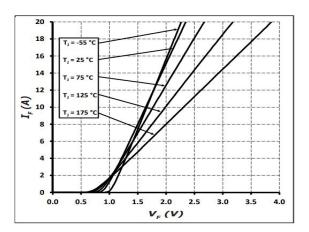


Figure 1Forward Characteristics

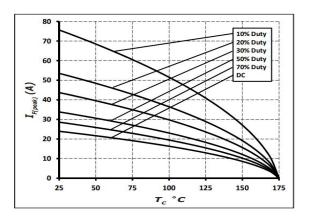


Figure 3Current Derating

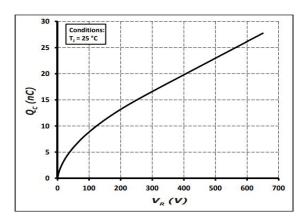


Figure 5Total Capacitance Charge vs. Reverse Voltage

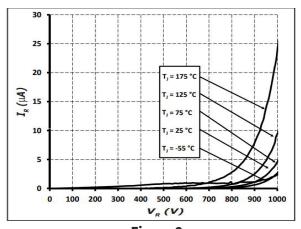


Figure 2Reverse Characteristics

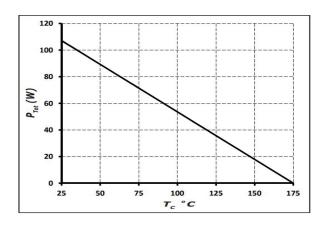


Figure 4 Power Derating

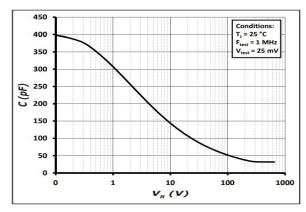
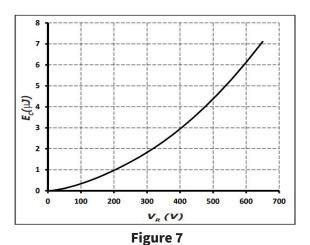


Figure 6Capacitance vs. Reverse Voltage

Typical Performance



Capacitance Stored Energy

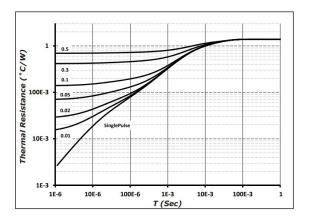
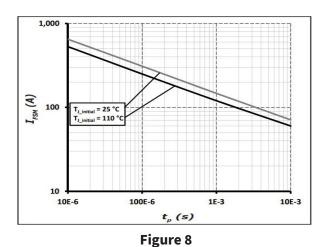


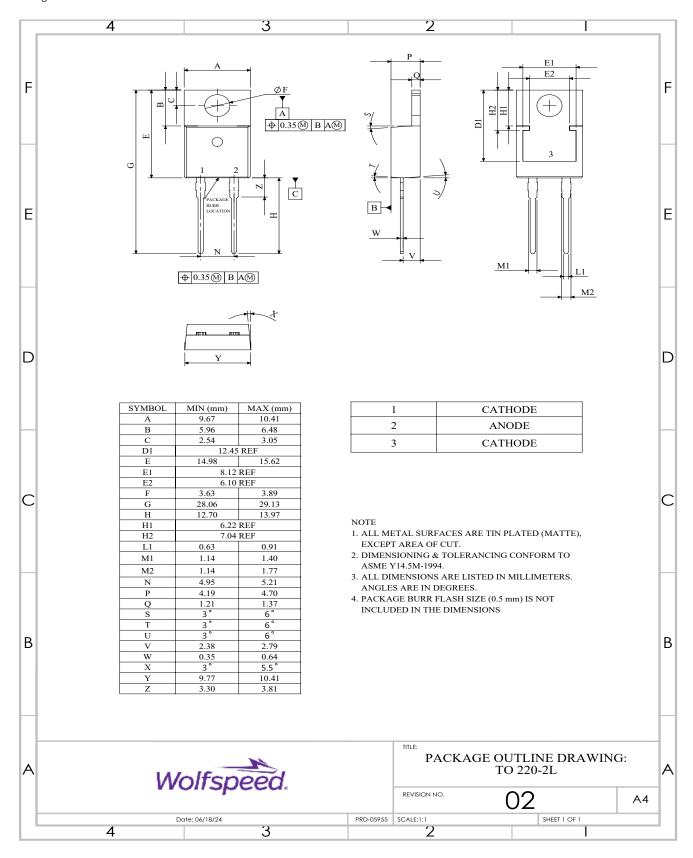
Figure 9Transient Thermal Impedance



Non-Repetitive Peak Forward Surge Current vs. Pulse Duraion

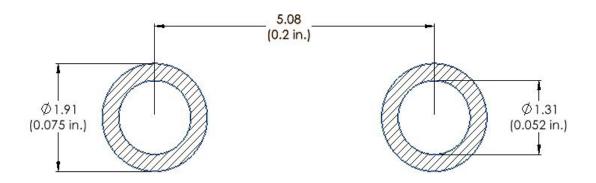
Package Dimensions & Pin-Out

Package: TO-220-2



Recommended Solder Pad Layout

Primary dimensions shown in mm. Learn more about recommended soldering profiles in this application note.



Product Ordering Information

Order Number	Packing Type
C3D08060A	Tube

Learn more about power device packing & shipment information in this application note.

Revision History

Document Version	Date of Release	Description of Changes
1	August-2016	Initial Release
8	January-2023	Update Package Drawing Update Landing Pad
9	July-2023	Updated Test Conditions of I _F and P _{TOT} Added Package Marking Statement
10	October - 2024	Legal disclaimer, POD, corrected package marking



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