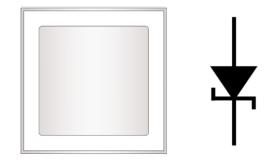


# CPW4-1200-S010B

# Gen 4 Silicon Carbide Schottky Diode

### Description

This is the 4th generation of high voltage, high performance Z-Rec<sup>®</sup> silicon carbide Schottky diode in a packageless bare die format to be implemented into any custom module design. The lower forward voltage, smaller reverse leakage current, zero reverse recovery, and high thermal conductivity make this Schottky diode ideal for high frequency switching applications including solar inverters and motor drives. This Schottky diode can be used in conjunction with either IGBT or MOSFET as an anti-parallel diode, or as a rectifier.



Package Type: Bare Die PN's: CPW4-1200-S010B

#### **Features**

- 1200V Schottky Rectifier
- Zero Reverse Recovery
- Zero Forward Recovery
- High-Frequency Operation
- Temperature-Independent Switching Behavior
- Extremely Fast Switching
- Positive Temperature Coefficient on V<sub>F</sub>

## Applications

- Solar Inverters
- Motor Drives
- EV Chargers
- UPS
- Industrial Power Supplies

#### Absolute Maximum Ratings Stress beyond those listed under absolute maxir

Stress beyond those listed under absolute maximum ratings may damage the device.

Parameter	Symbol		Rating	Unit
Repetitive Peak Reverse Voltage	V <sub>RRM</sub>		1200	V
		T <sub>c</sub> = 25°C	33	A
Continuous Forward Current	I <sub>F</sub>	T <sub>c</sub> = 135°C	16	
		T <sub>c</sub> = 156°C	10	
Repetitive Peak Forward Surge Current, assumes t <sub>p</sub> = 10ms,	1	T <sub>c</sub> = 25°C	47	A
Half Sine Wave Pulse	IFRM	$T_{c} = 110^{\circ}C$	31.5	
Non-Repetitive Forward Surge Current, assumes $t_p$ = 10ms,		$T_c = 25^{\circ}C$	71	A
Half Sine Wave Pulse	IFSM	$T_c = 110^{\circ}C$	59.5	
Virtual Junction and Storage Temperature	T <sub>VJ</sub> , T <sub>stg</sub>		-55 to +175	°C
Maximum Processing Temperature, in non-reactive ambient	T <sub>proc</sub>		325	°C

Note: All above notation to  $T_c$  specifies case temperature from die packaged in TO-247, with Rth(j-c) <  $0.9^{\circ}C/W$ 

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# Electrical Characteristics ( $T_{vJ} = 25$ °C)

Parameter	Symbol	Тур.	Max.	Unit	Test Conditions
Forward Voltage		1.5	1.8	- V	I <sub>F</sub> = 10 A
Forward Voltage	V <sub>f</sub>	2.2	3		I <sub>F</sub> = 10 A, T <sub>VJ</sub> = 175°C
Deverse Current		30	250	μA	V <sub>R</sub> = 1200 V
Reverse Current	IR	55	350		V <sub>R</sub> = 1200 V, T <sub>VJ</sub> = 175°C
Total Capacitive Charge	Qc	52		nC	$V_R = 800 \text{ V}, I_F = 10 \text{ A}, \text{ di/dt} = 200 \text{ A/}\mu\text{s}$
Total Capacitance		754		pF	$V_R = 0 V$ , f = 1Mhz
	С	45			V <sub>R</sub> = 400 V, f = 1Mhz
		38			V <sub>R</sub> = 800 V, f = 1Mhz
Capacitance Stored Energy	Ec	14.5		μJ	V <sub>R</sub> = 400 V

## **Thermal Characteristics**

Parameter	Symbol	Typical	Unit
Thermal Resistance from Junction to Case <sup>1</sup>	R <sub>th(j-c)</sub>	0.9	°C/W

Note:

<sup>1</sup>Tested in TO-247 Package

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## CPW4-1200-S010B

#### **Typical Performance**

All the graphs are based on a die placed in a TO-247 package.

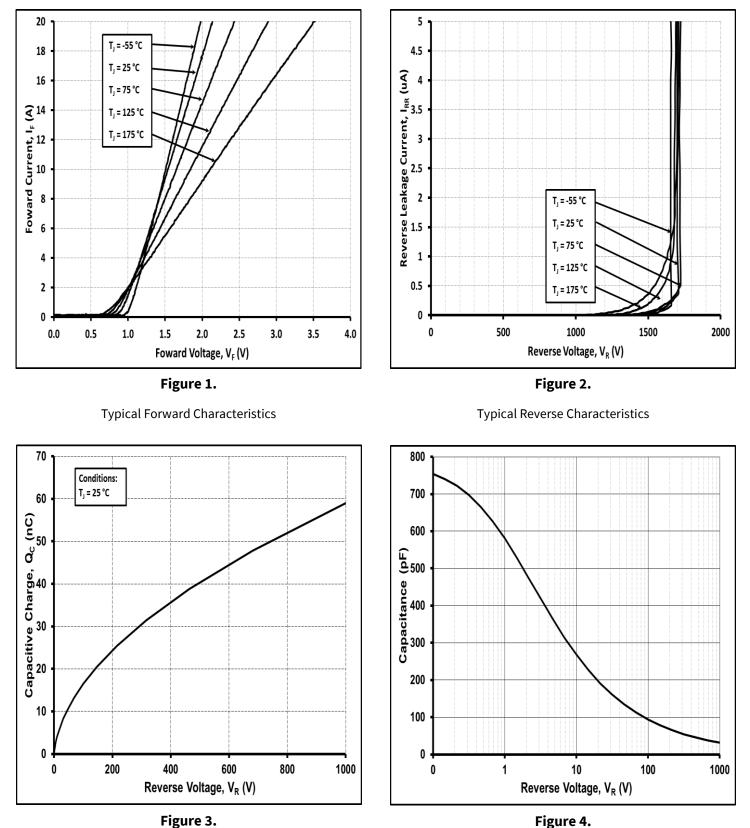
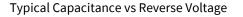


Figure 3.



Typical Recovery Charge vs Reverse Voltage

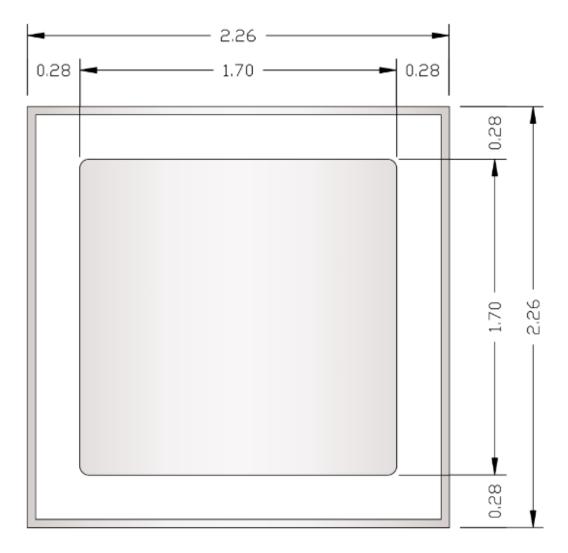
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## Product Dimensions CPW4-1200-S010B



## Product Dimensions CPW4-1200-S010B

Parameter	Typical	Units
Die Size (L x W)	2.26 x 2.26	mm
Anode Pad Opening	1.70 x 1.70	mm
Die Thickness <sup>1</sup>	377 ± 10%	μm
Topside Anode Metalization (Al)	4	μm
Backside Cathode Metalization (Ni/Ag)	1.4	μm
Frontside Passivation (polymide)	Polyimide	

<sup>1</sup>SiC Thickness

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## **Product Ordering Information**

Order Number	ber Description Packag	
CPW4-1200-S010B-FU6	SiC Diode G4 IND 1200V/10A FULL MLT	Bare Die Product

## **Revision History**

<b>Revision History</b>	Date of Change	Brief Summary
3		Initial Release
4	9/30/2023	Template updated

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