

# **CGHV22200**

## 200 W, 1800-2200 MHz, GaN HEMT for LTE

Cree's CGHV22200 is a gallium nitride (GaN) high electron mobility transistor (HEMT) is designed specifically for high efficiency, high gain and wide bandwidth capabilities, which makes the CGHV22200F ideal for 1.8 - 2.2 GHz LTE, 4G Telecom and BWA amplifier applications. The transistor is input matched and supplied in a ceramic/metal flange package.



Package Type: 440162 and 440161 PN: CGHV22200F and CGHV222200P

# Typical Performance Over 1.8 - 2.2 GHz ( $T_c$ = 25°C) of Demonstration Amplifier

Parameter	1.8 GHz	2.0 GHz	2.2 GHz	Units
Gain @ 47 dBm	16.6	19.2	18.1	dB
ACLR @ 47 dBm	-37.4	-37.4	-35.6	dBc
Drain Efficiency @ 47 dBm	31.5	31.9	34.8	%

#### Note:

Measured in the CGHV22200-AMP amplifier circuit, under WCDMA 3GPP test model 1, 64 DPCH, 45% clipping, PAR = 7.5 dB @ 0.01% Probability on CCDF.  $I_{\rm ps}$  = 1.0 A

### **Features**

ROHS

- 1.8 2.2 GHz Operation
- 18 dB Gain
- 35 dBc ACLR at 50 W  $P_{\text{AVE}}$
- 31-35 % Efficiency at 50 W P<sub>AVE</sub>
- High Degree of DPD Correction Can be Applied

Large Signal Models Available for ADS and MWO



## Absolute Maximum Ratings (not simultaneous) at 25°C Case Temperature

Parameter	Symbol	Rating	Units	Conditions
Drain-Source Voltage	V <sub>DSS</sub>	125	Volts	25°C
Gate-to-Source Voltage	V <sub>GS</sub>	-10, +2	Volts	25°C
Storage Temperature	T <sub>STG</sub>	-65, +150	°C	
Operating Junction Temperature <sup>3</sup>	T <sub>J</sub>	225	°C	
Maximum Forward Gate Current	I <sub>GMAX</sub>	32	mA	25°C
Maximum Drain Current <sup>1</sup>	I <sub>DMAX</sub>	12	А	25°C
Soldering Temperature <sup>2</sup>	T <sub>s</sub>	245	°C	
Screw Torque	τ	80	in-oz	
Thermal Resistance, Junction to Case <sup>3</sup>	R <sub>eJC</sub>	1.22	°C/W	85°C, P <sub>DISS</sub> = 96 W
Thermal Resistance, Junction to Case <sup>4</sup>	R <sub>eJC</sub>	1.54	°C/W	85°C, P <sub>DISS</sub> = 96 W
Case Operating Temperature <sup>5</sup>	T <sub>c</sub>	-40, +150	°C	

### Note:

# Electrical Characteristics (T<sub>c</sub> = 25°C)

Characteristics	Symbol	Min.	Тур.	Max.	Units	Conditions
DC Characteristics <sup>1</sup>						
Gate Threshold Voltage	$V_{\rm GS(th)}$	-3.8	-3.0	-2.3	V <sub>DC</sub>	$V_{DS} = 10 \text{ V, } I_{D} = 32 \text{ mA}$
Gate Quiescent Voltage	$V_{\rm GS(Q)}$	-	-2.7	-	V <sub>DC</sub>	$V_{DS} = 50 \text{ V, } I_{D} = 1.0 \text{ A}$
Saturated Drain Current <sup>2</sup>	I <sub>DS</sub>	24	28.8	-	Α	$V_{DS} = 6.0 \text{ V, } V_{GS} = 2.0 \text{ V}$
Drain-Source Breakdown Voltage	$V_{\rm BR}$	150	-	-	V <sub>DC</sub>	$V_{GS} = -8 \text{ V, } I_{D} = 32 \text{ mA}$
RF Characteristics $^3$ (T $_c$ = 25 $^\circ$ C, F $_0$ = 2.17 GH	Iz unless other	wise noted)				
Saturated Output Power <sup>3,4</sup>	$P_{SAT}$	-	240	-	W	$V_{DD} = 50 \text{ V, } I_{DQ} = 1.0 \text{ A}$
Pulsed Drain Efficiency <sup>3</sup>	η	-	65	-	%	$V_{DD} = 50 \text{ V, } I_{DQ} = 1.0 \text{ A, } P_{OUT} = P_{SAT}$
Gain <sup>6</sup>	G	-	18.0	-	dB	$V_{DD} = 50 \text{ V, } I_{DQ} = 1.0 \text{ A, } P_{OUT} = 47 \text{ dBm}$
WCDMA Linearity <sup>6</sup>	ACLR	-	-36.7	-	dBc	$V_{DD} = 50 \text{ V, } I_{DQ} = 1.0 \text{ A, } P_{OUT} = 47 \text{ dBm}$
Drain Efficiency <sup>6</sup>	η	-	34.5	-	%	$V_{DD} = 50 \text{ V, } I_{DQ} = 1.0 \text{ A, } P_{OUT} = 47 \text{ dBm}$
Output Mismatch Stress <sup>3</sup>	VSWR	-	-	10:1	Ψ	No damage at all phase angles, $V_{DD}$ = 50 V, $I_{DQ}$ = 1.0 A, $P_{OUT}$ = 200 W Pulsed
Dynamic Characteristics						
Input Capacitance <sup>7</sup>	C <sub>GS</sub>	-	97	-	pF	$V_{DS} = 50 \text{ V, } V_{gs} = -8 \text{ V, } f = 1 \text{ MHz}$
Output Capacitance <sup>7</sup>	C <sub>DS</sub>	-	13.4	-	pF	$V_{DS} = 50 \text{ V}, V_{gs} = -8 \text{ V}, f = 1 \text{ MHz}$
Feedback Capacitance	$C_{GD}$	-	0.94	-	pF	$V_{DS} = 50 \text{ V, } V_{gs} = -8 \text{ V, } f = 1 \text{ MHz}$

### Notes:

<sup>&</sup>lt;sup>1</sup> Current limit for long term, reliable operation.

<sup>&</sup>lt;sup>2</sup> Refer to the Application Note on soldering at <a href="http://www.cree.com/rf/document-library">http://www.cree.com/rf/document-library</a>

<sup>&</sup>lt;sup>3</sup> Measured for the CGHV22200P

<sup>&</sup>lt;sup>4</sup> Measured for the CGHV22200F

<sup>&</sup>lt;sup>5</sup> See also, the Power Dissipation De-rating Curve on Page 6.

<sup>&</sup>lt;sup>1</sup> Measured on wafer prior to packaging.

<sup>&</sup>lt;sup>2</sup> Scaled from PCM data.

 $<sup>^3</sup>$  Pulse Width = 100  $\mu$ S, Duty Cycle = 10%

 $<sup>^4</sup>$  P<sub>SAT</sub> is defined as I<sub>G</sub> = 3 mA peak.

<sup>&</sup>lt;sup>5</sup> Measured in CGHV22200-AMP

<sup>&</sup>lt;sup>6</sup> Single Carrier WCDMA, 3GPP Test Model 1, 64 DPCH, 45% Clipping, PAR = 7.5 dB @ 0.01% Probability on CCDF.

<sup>&</sup>lt;sup>7</sup> Includes package and internal matching components.



Figure 1. - Small Signal Gain and Return Losses vs Frequency for the CGHV22200 measured in CGHV22200-AMP Amplifier Circuit  $V_{\rm DD}$  = 50 V,  $I_{\rm DQ}$  = 1.0 A

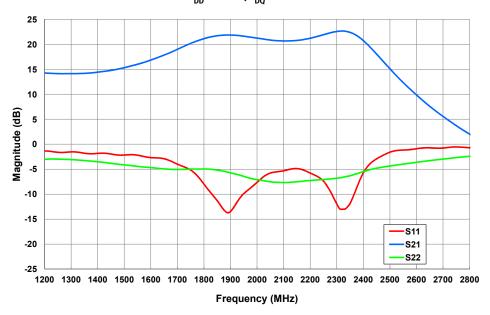


Figure 2. - Typical Gain and Drain Efficiency vs Input Power of the CGHV22200 measured in CGHV22200-AMP Amplifier Circuit.  $V_{DS}$  = 50 V,  $I_{DQ}$  = 1.0 A, Freq = 2.1 GHz, Pulse Width = 100  $\mu$ s, Duty Cycle = 10 %

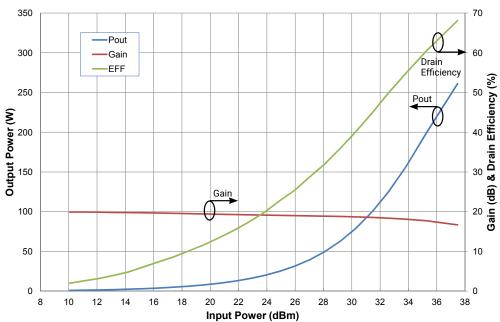




Figure 3. - Typical WCDMA Transfer Characteristics  $V_{\rm DD}$  = 50 V,  $I_{\rm DS}$  = 1.0 A, 1c WCDMA, PAR = 7.5 dB

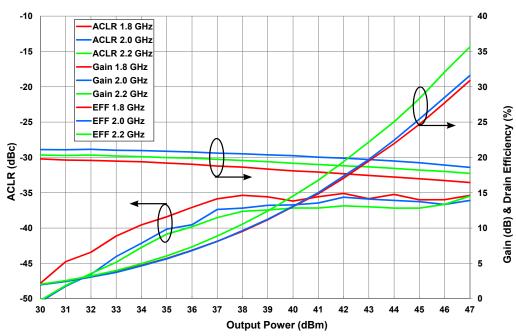


Figure 4. - Typical Gain, Drain Efficiency and ACLR vs Frequency of the CGHV22200 measured in CGHV22200-AMP Amplifier Circuit  $V_{DD}$  = 50 V,  $I_{DS}$  = 1.0 A,  $P_{AVE}$  = 50 W, 1c WCDMA, PAR = 7.5 dB

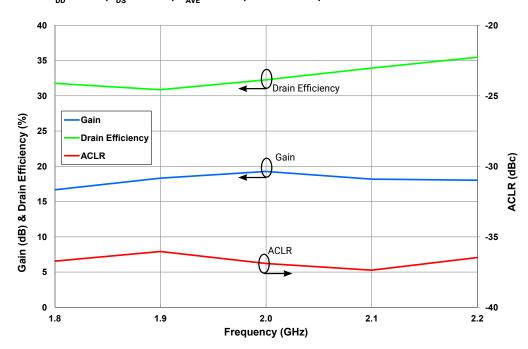




Figure 5. - CGHV22200 Spectral Mask at  $P_{AVE}$  = 47 dBm with and without DPD  $V_{DD}$ =50,  $I_{DQ}$ =1.0 A, Freq=2.14 GHz, 1 C WCDMA 7.5 PAR

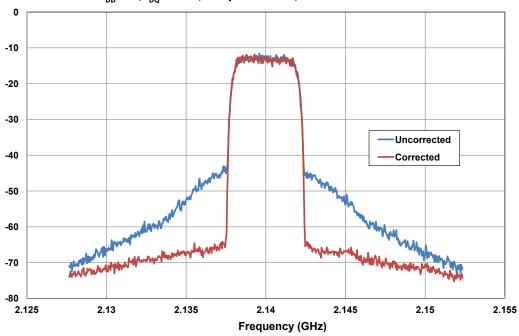


Figure 6. - CGHV22200 Typical Linearity under DPD vs. Output Power  $V_{\rm DD}$ =50,  $I_{\rm DO}$ =1.0 A, Freq=2.14 GHz, 1 C WCDMA 7.5 PAR

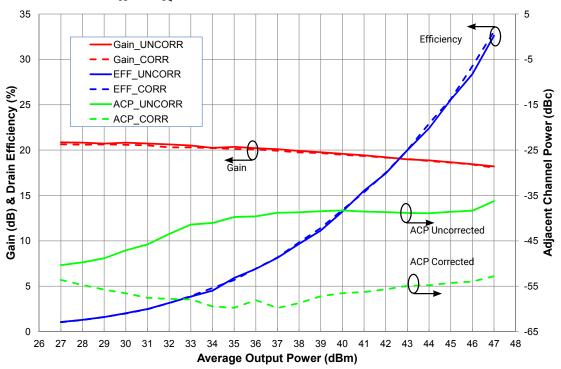




Figure 7. - Intermodulation Distortion Products vs Output Power Freq. = 2.1 GHz, VDD = 50 V, IDQ = 1.0 A, Tone Spacing = 100 kHz.

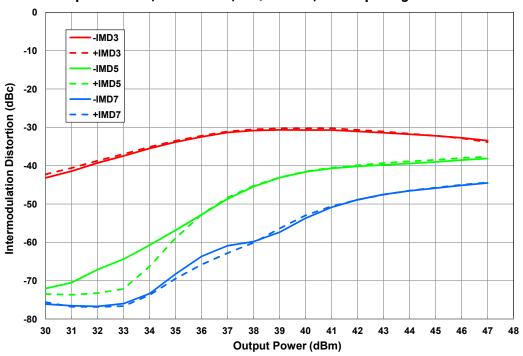
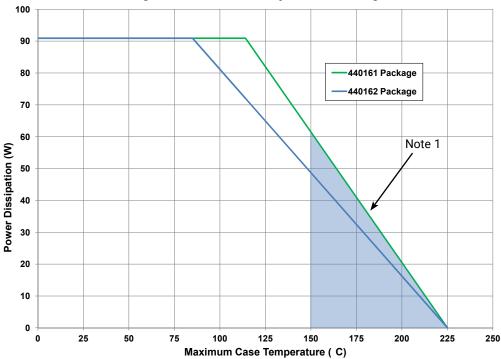


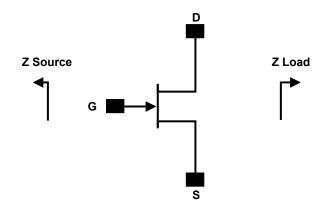
Figure 8. - Power Dissipation Derating Curve



Note 1. Area exceeds Maximum Case Operating Temperature (See Page 2).



# **Source and Load Impedances**



Frequency (MHz)	Z Source	Z Load
1800	10.6 - j7.3	2.7 + j0.6
1900	8.1 - j7.4	2.8 + j0.7
2000	6.1 - j6.6	2.9 + j0.8
2100	4.7 - j5.5	2.8 + j0.8
2200	3.7 - j4.3	2.6 + j0.8

Note¹:  $V_{DD}$  = 50 V,  $I_{DQ}$  = 1.0 A. In the 440162 package. Note²: Impedances are extracted from CGHV22200-AMP demonstration

circuit and are not source and load pull data derived from transistor.

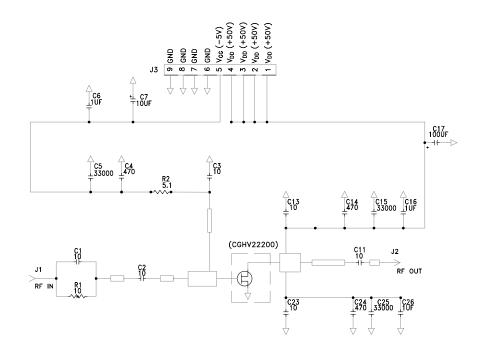


# **CGHV22200-AMP Demonstration Amplifier Circuit Bill of Materials**

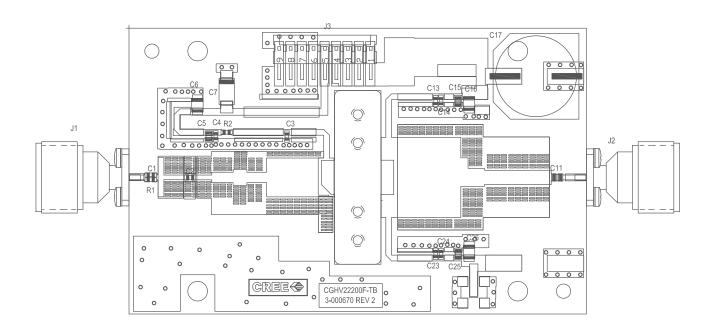
Designator	Description	Qty
R1	RES, 1/16 W, 0603, 1%, 10.0 OHMS	1
R2	RES, 1/16 W, 0603, 1%, 5.1 OHMS	1
C4, C14, C24	CAP, 470 pF, 5%, 100 V, 0603, X	3
C6,C16, C26	CAP, 1.0 UF, 100 V, 10%, x7R, 121	3
C17, C27	CAP, 100 UF, 20%, 160 V, ELEC	2
C7	CAP, 10 UF, 16 V, TANTALUM, 2312	1
C1, C2, C3, C13, C23	CAP, 10.0 pF, 5%, 0603, ATC	5
C5, C15, C25	CAP, 33000 pF, 0805, 100 V, X7R	3
C11	CAP, 10 pF, 5%, 250 V, 0805, A	1
J1, J2	CONN, N, FEM, W/.500 SMA FLNG	2
J3	HEADER RT>PLZ .1CEN LK 9POS	1
	PCB, CGHV22200F, RO4350,0.020" THK	1
	2-56 SOC HD SCREW 1/4 SS	4
	#2 SPLIT LOCKWASHER SS	4
	CGHV22200	1



### **CGHV22200-AMP Demonstration Amplifier Circuit Schematic**

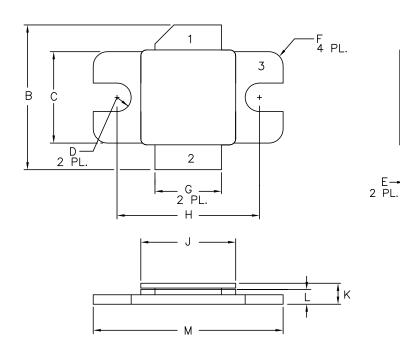


### **CGHV22200-AMP Demonstration Amplifier Circuit Outline**





## Product Dimensions CGHV22200F (Package Type - 440162)



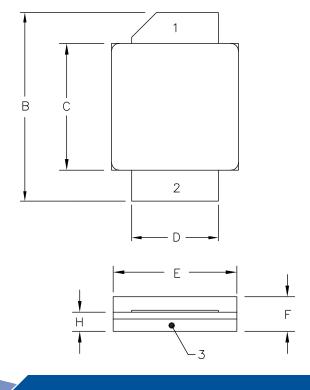
#### NOTES:

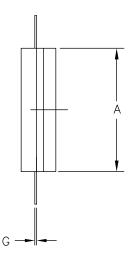
- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.
- 3. ADHESIVE FROM LID MAY EXTEND A MAXIMUM OF 0.020" BEYOND EDGE OF LID.
- LID MAY BE MISALIGNED TO THE BODY
  OF THE PACKAGE BY A MAXIMUM OF 0.008" IN
  ANY DIRECTION.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	.395	.405	10.03	10.29
В	.580	.620	14.73	15.75
С	.380	.390	9.65	9.91
D	.055	.065	1.40	1.65
E	.004	.006	0.10	0.15
F	.055	.065	1.40	1.65
G	.275	.285	6.99	7.24
Н	.595	.605	15.11	15.37
J	.395	.405	10.03	10.29
K	.129	.149	3.28	3.78
L	.053	.067	1.35	1.70
М	.795	.805	20.19	20.45

PIN 1. GATE PIN 2. DRAIN PIN 3. SOURCE

## Product Dimensions CGHV22200P (Package Type - 440161)





PIN 1. GATE PIN 2. DRAIN PIN 3. SOURCE

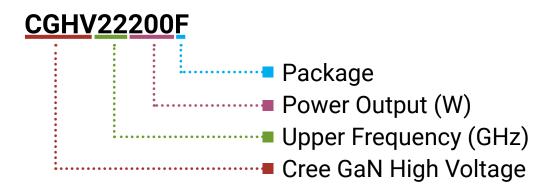
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	INCHES		MILLIM	IETERS
DIM	MIN	MAX	MIN	MAX
Α	.395	.407	10.03	10.34
В	.594	.634	15.09	16.10
С	.395	.407	10.03	10.34
D	.275	.285	6.99	7.24
E	.395	.407	10.03	10.34
F	.129	.149	3.28	3.78
G	.004	.006	0.10	0.15
Н	.057	.067	1.45	1.70



## **Part Number System**



Parameter	Value	Units	
Upper Frequency <sup>1</sup>	2.2	GHz	
Power Output	200	W	
Package	Flange	-	

Table 1.

**Note**<sup>1</sup>: Alpha characters used in frequency code indicate a value greater than 9.9 GHz. See Table 2 for value.

Character Code	Code Value
А	0
В	1
С	2
D	3
E	4
F	5
G	6
Н	7
J	8
K	9
Examples:	1A = 10.0 GHz 2H = 27.0 GHz

Table 2.



# **Product Ordering Information**

Order Number	Description	Unit of Measure	lmage
CGHV22200F	GaN HEMT	Each	CREE COPY CCH V22200F CCH V22200F CC C780 V25
CGHV22200P	GaN HEMT	Each	CREE COP CGHV22200P CGHV22200P
CGHV22200-TB	Test board without GaN HEMT	Each	
CGHV22200F-AMP	Test board with GaN HEMT installed	Each	



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